

## BUS RECEIVER



FOE280 0522h260

FIG.2

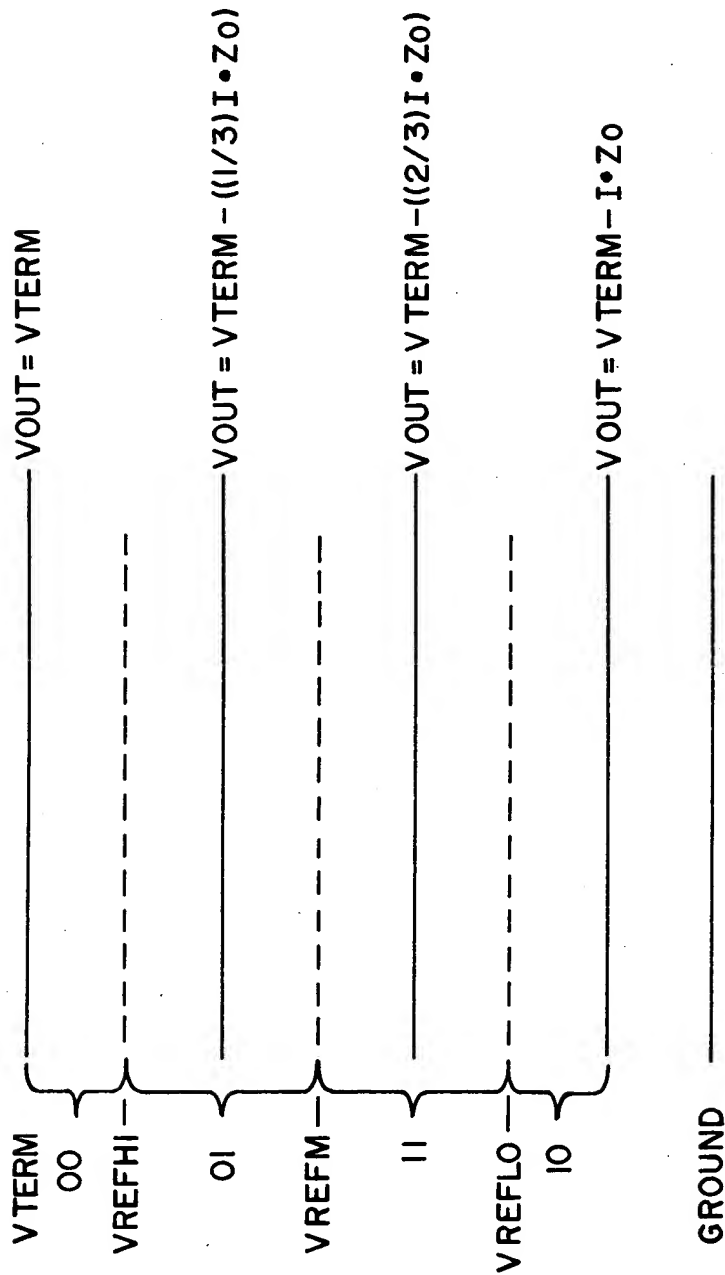


FIG. 3A

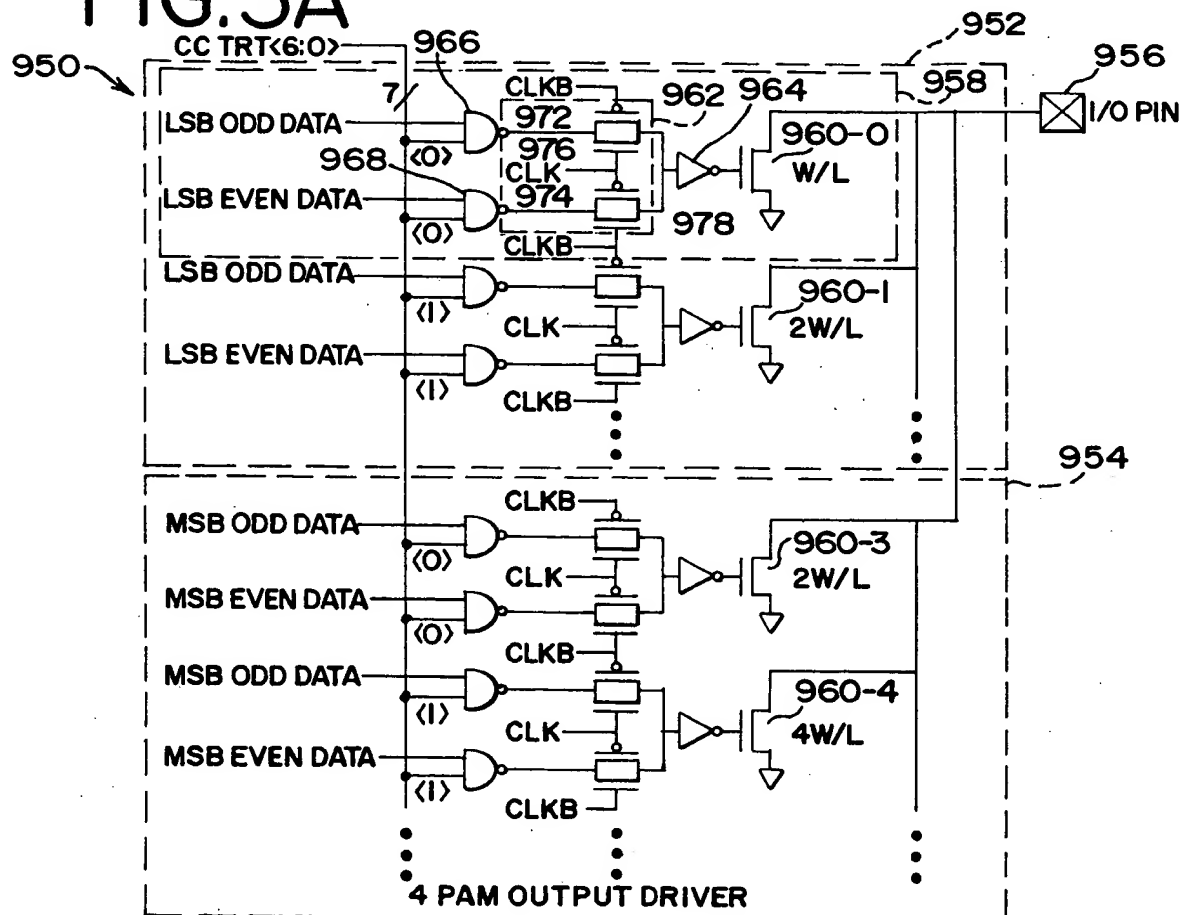


FIG. 3B

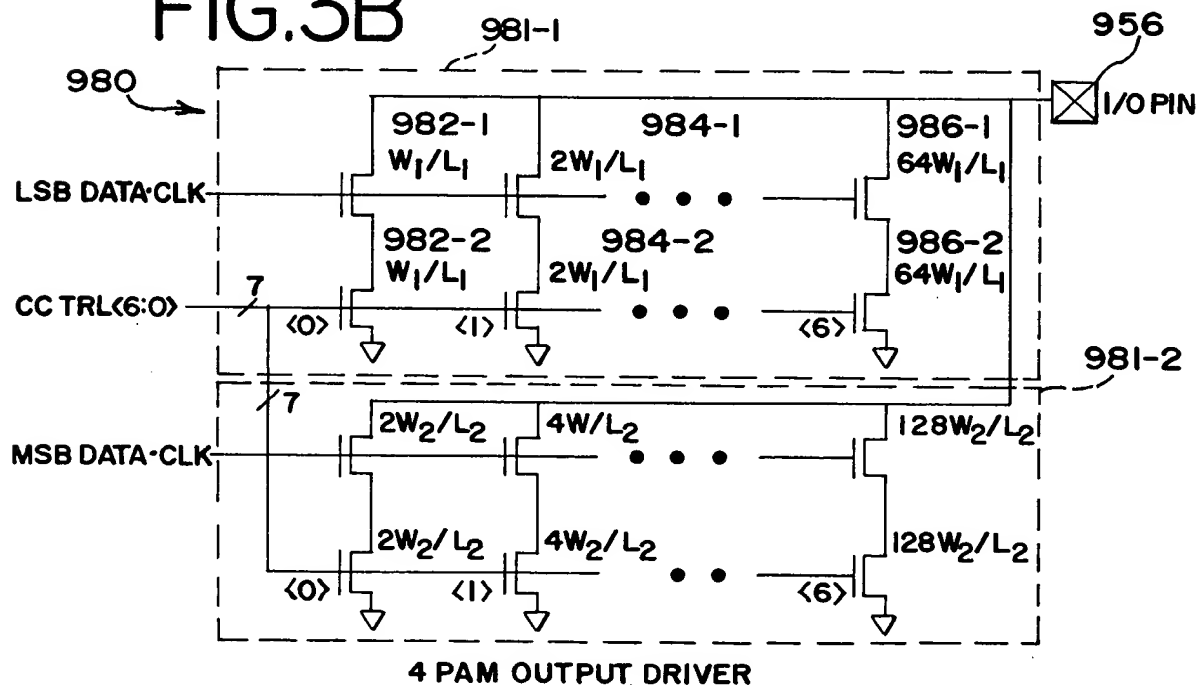


FIG. 4A

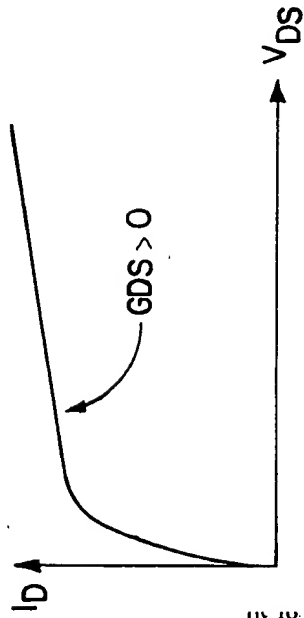


FIG. 4B

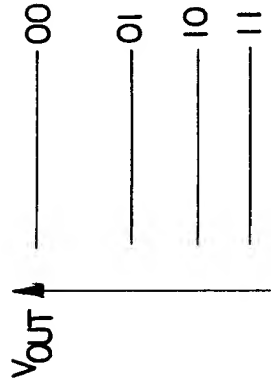


FIG. 4C

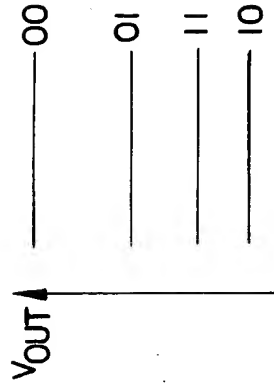


FIG.5A

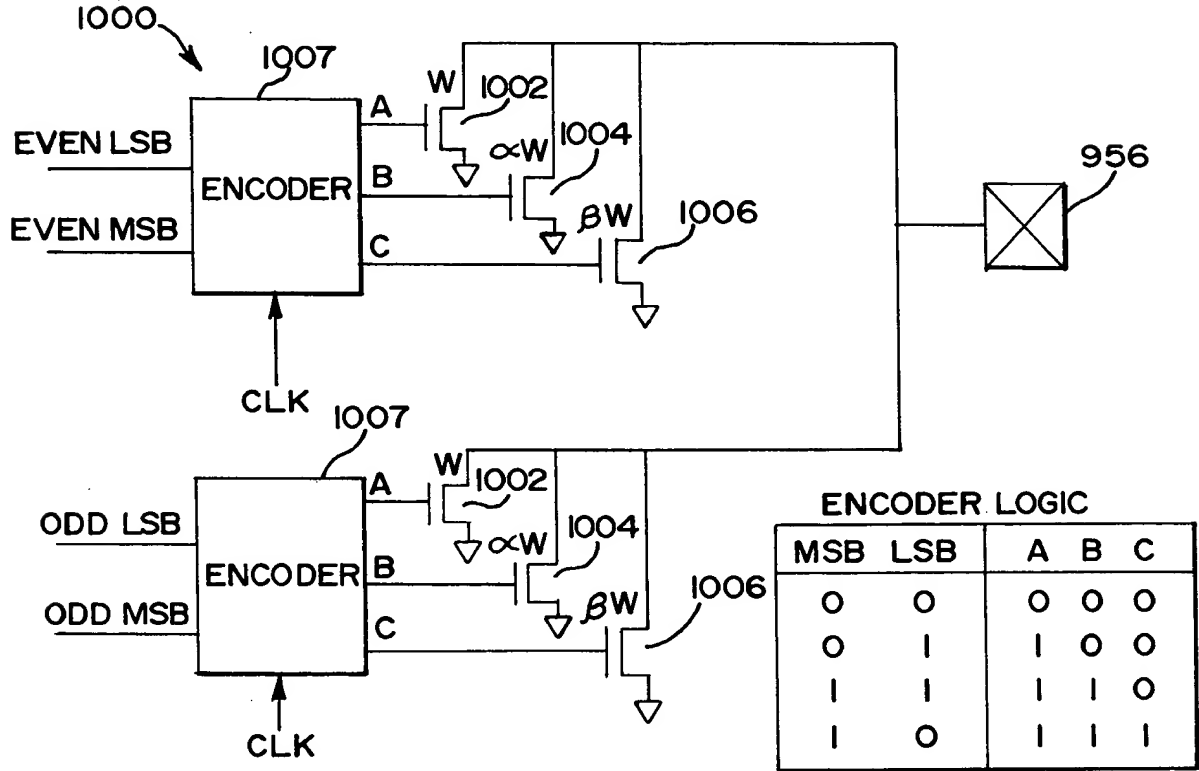


FIG.5B

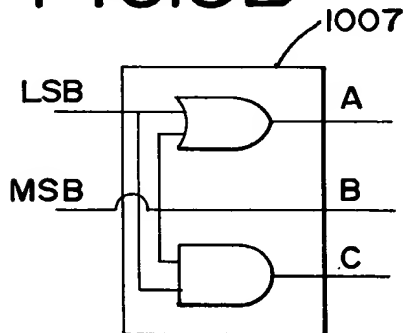


FIG.5C

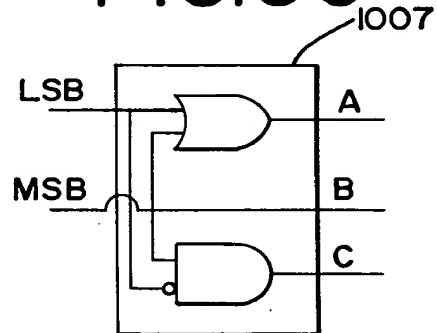
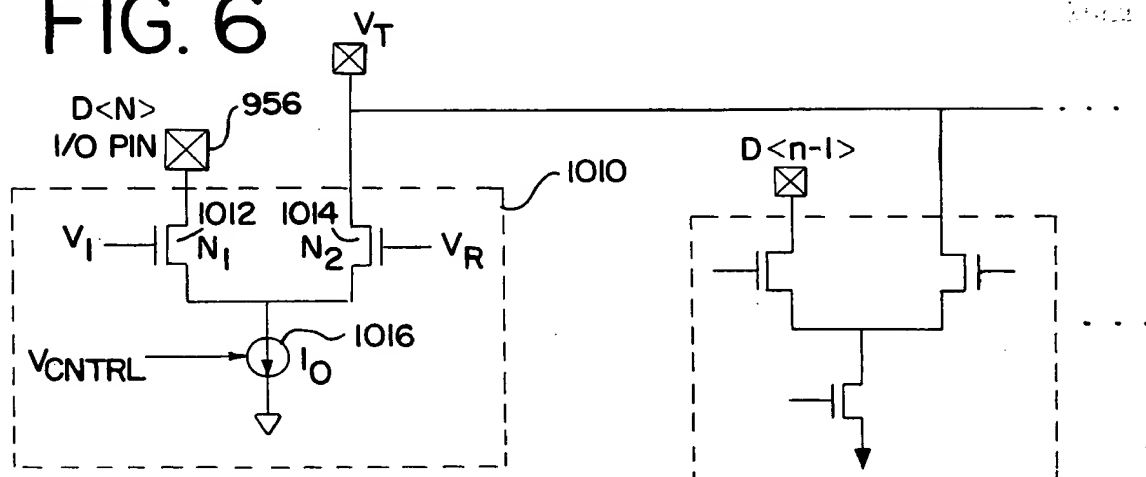


FIG. 6



CIRCUIT TO REDUCE SWITCHING NOISE

FIG. 7

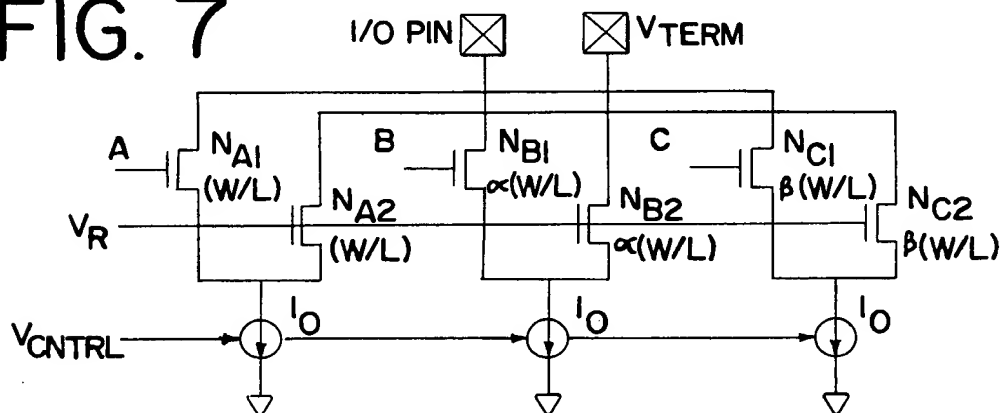
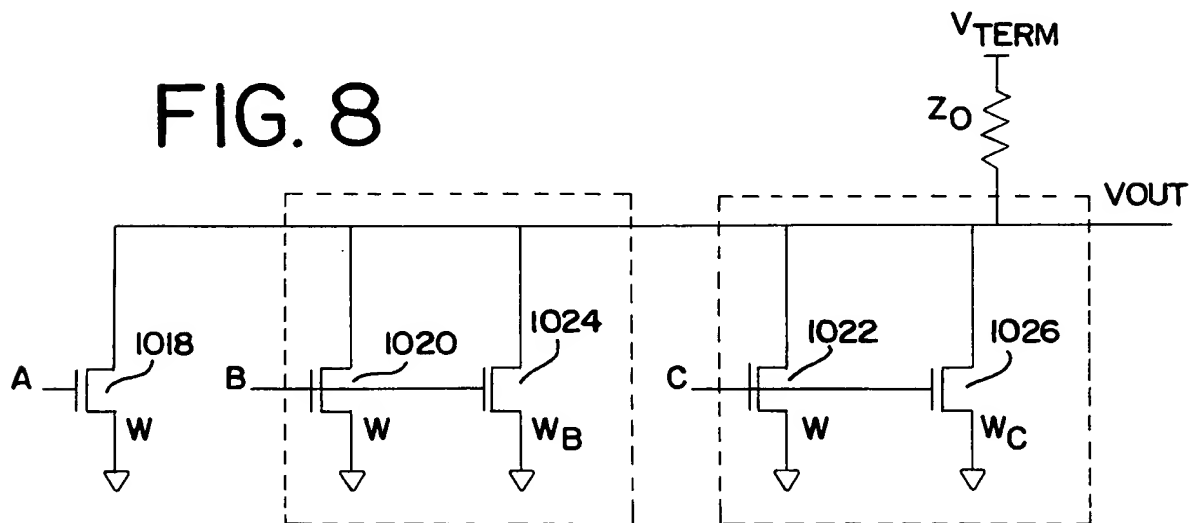
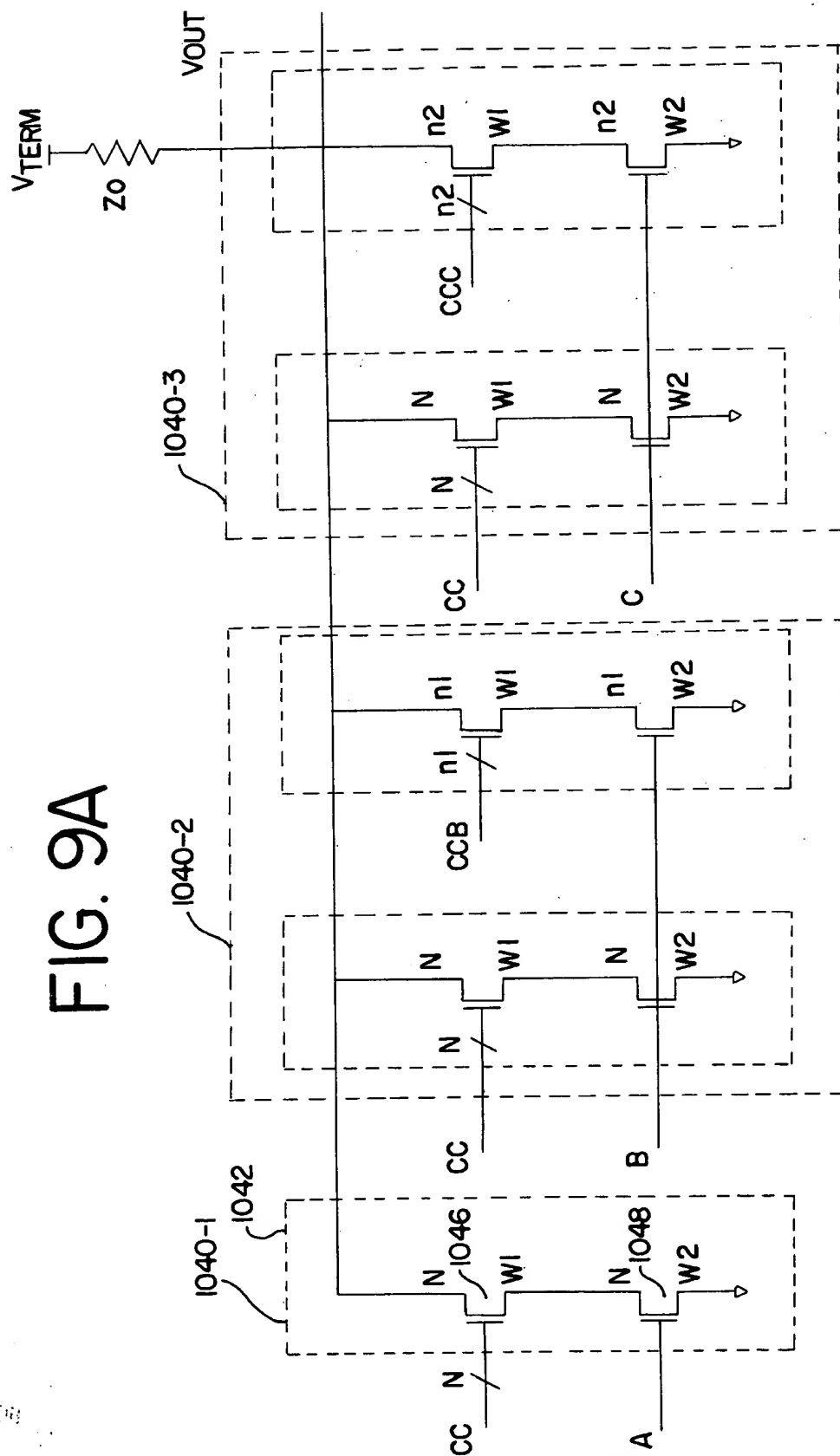


FIG. 8



GDS COMPENSATED MULTI-PAM OUTPUT DRIVER

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GDS COMPENSATED MULTI-PAM OUTPUT DRIVER WITH CURRENT CONTROL

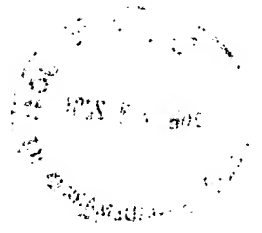


FIG. 9B

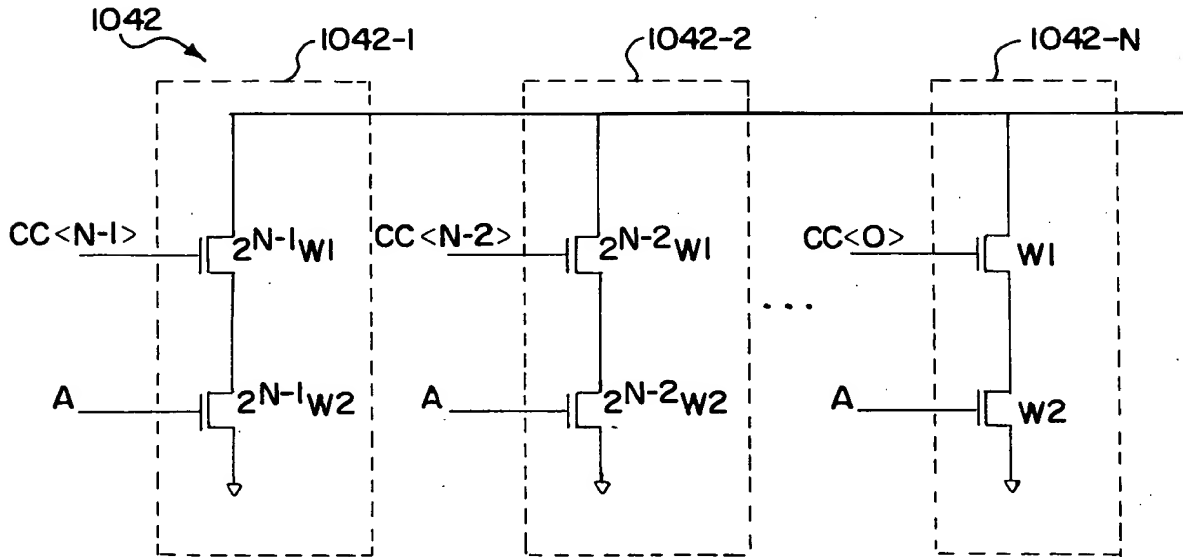
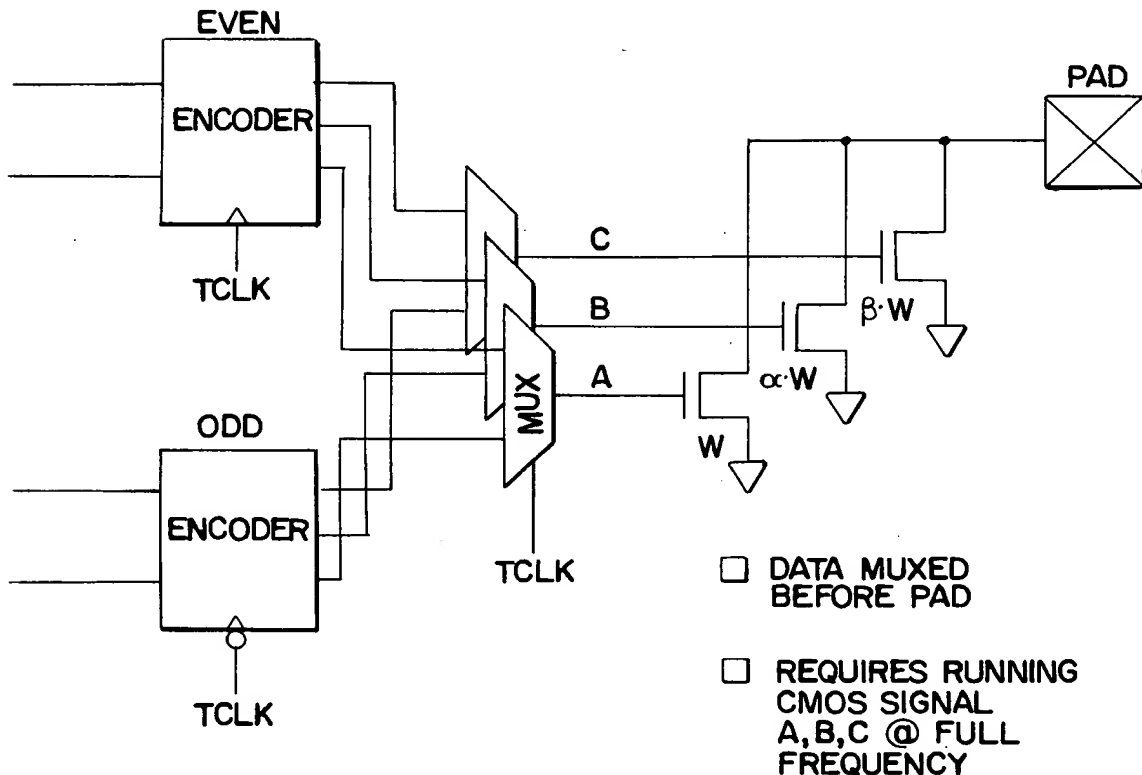


FIG. 9C



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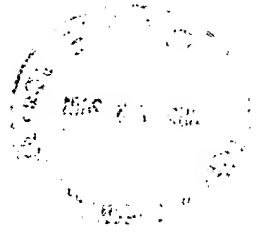
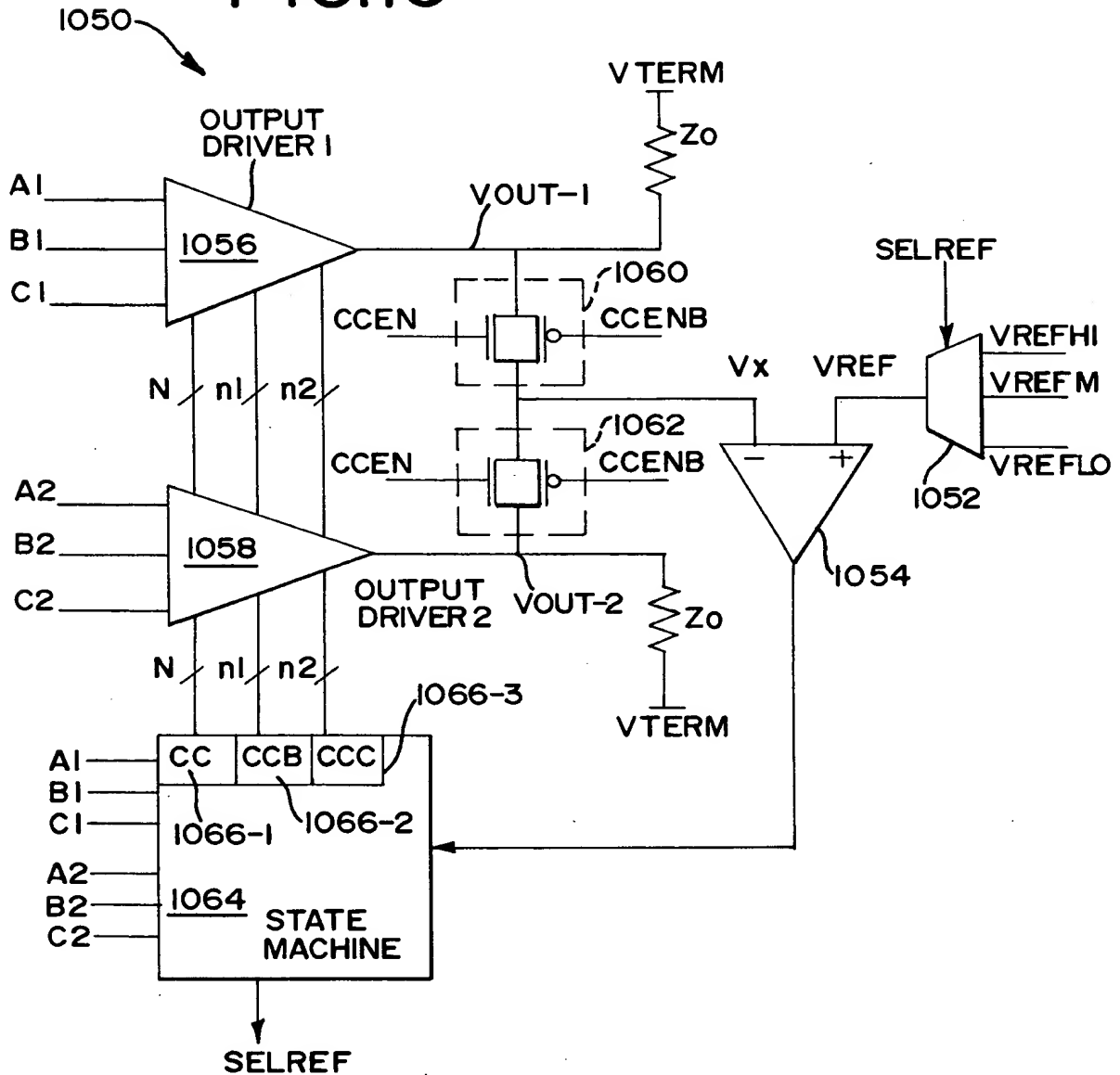


FIG.10



CIRCUIT FOR CALIBRATING THE GDS COMPENSATED  
OUTPUT DRIVER WITH CURRENT CONTROL

FIG. 11A

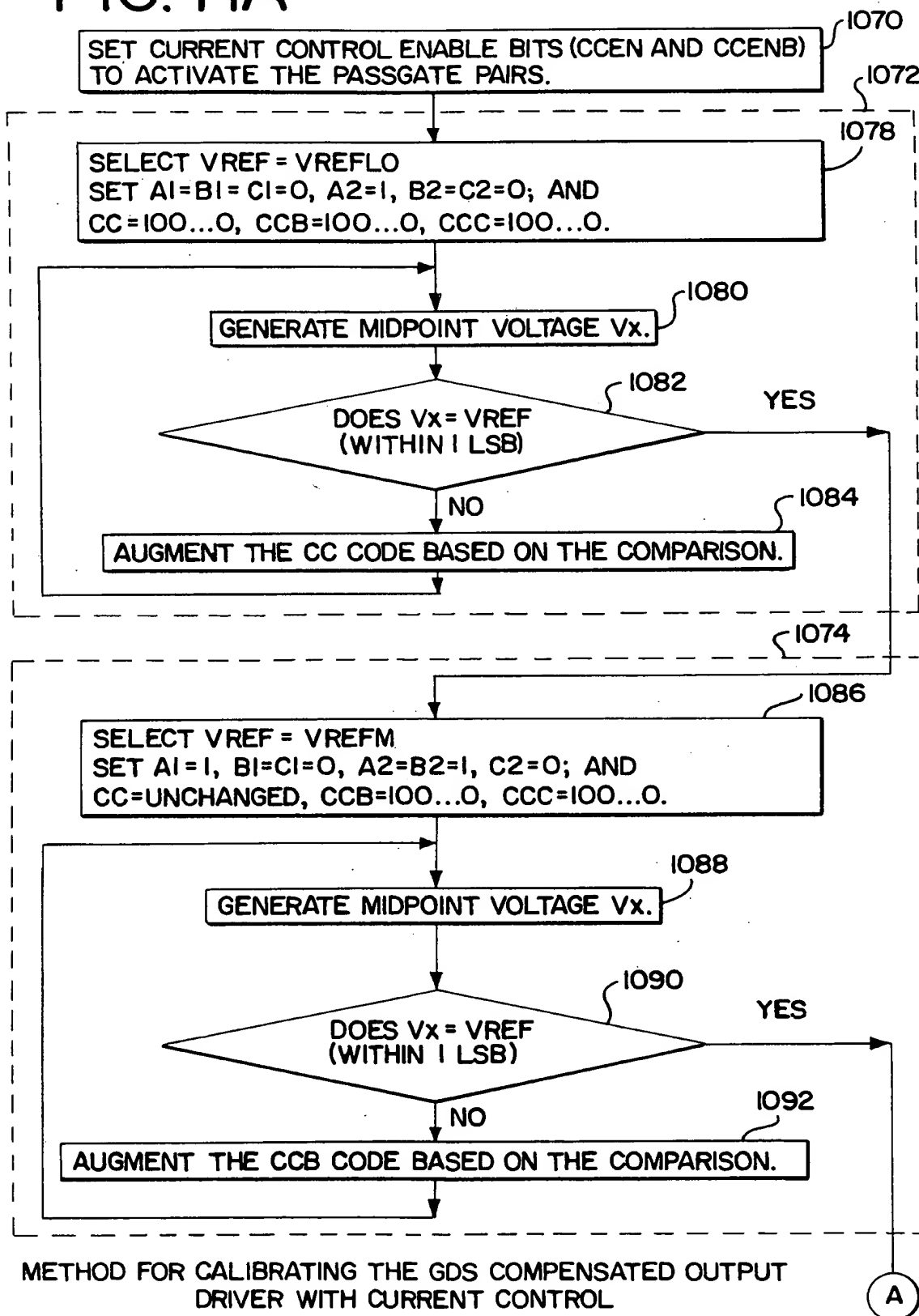
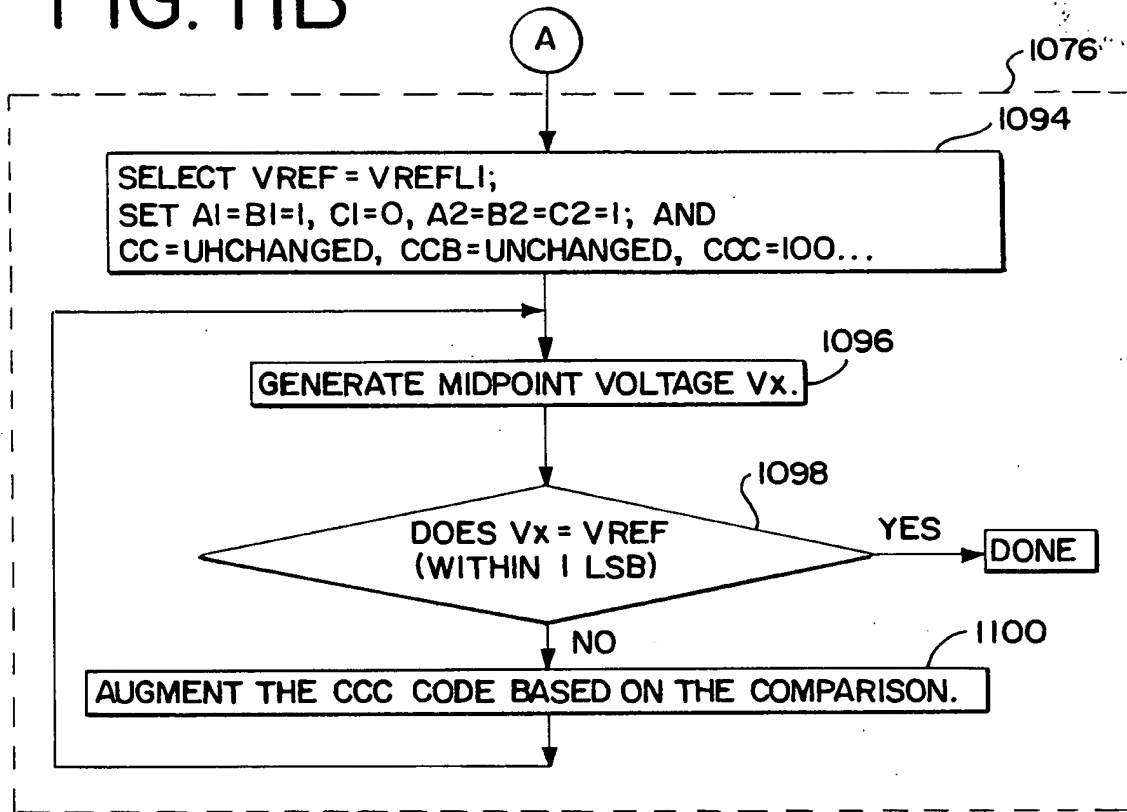
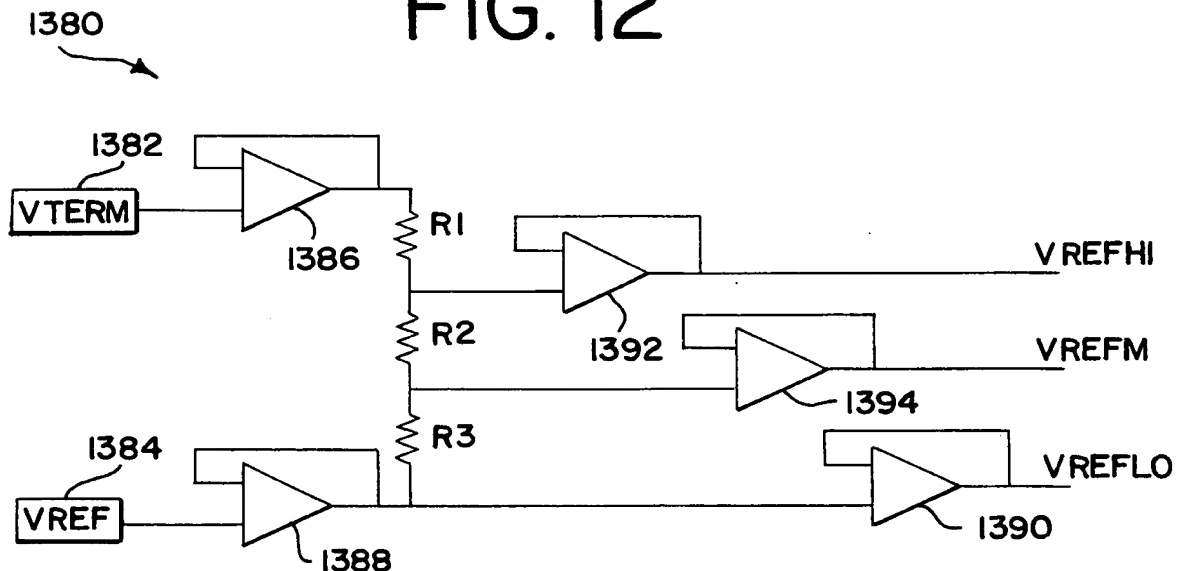


FIG. 11B



METHOD FOR CALIBRATING THE GDS COMPENSATED OUTPUT DRIVER WITH CURRENT CONTROL

FIG. 12



**FIG. 13A**

The diagram illustrates a complex digital circuit, identified as a nonoverlapping clock driver 1506. It features a central logic block with multiple inputs and outputs, including **VOSEN**, **AZ**, **CLKB**, **COMP**, **COMPB**, **VBIA**, **VT1**, **RCOMP**, **PAZ**, **VR1**, and **VR2**. The circuit is composed of numerous logic gates (AND, OR, NOT, NAND, NOR) and inverters, labeled with identifiers such as **U0** through **U31**, **U33**, **U34**, **U35**, **U36**, **U37**, **U38**, **U39**, **U40**, **U41**, **U42**, **U43**, **U44**, **U45**, **U46**, **U47**, **U48**, **U49**, **U50**, **U51**, **U52**, **U53**, **U54**, **U55**, **U56**, **U57**, **U58**, **U59**, **U60**, **U61**, **U62**, **U63**, **U64**, **U65**, **U66**, **U67**, **U68**, **U69**, **U70**, **U71**, **U72**, **U73**, **U74**, **U75**, **U76**, **U77**, **U78**, **U79**, **U80**, **U81**, **U82**, **U83**, **U84**, **U85**, **U86**, **U87**, **U88**, **U89**, **U90**, **U91**, **U92**, **U93**, **U94**, **U95**, **U96**, **U97**, **U98**, **U99**, **U100**, **U101**, **U102**, **U103**, **U104**, **U105**, **U106**, **U107**, **U108**, **U109**, **U110**, **U111**, **U112**, **U113**, **U114**, **U115**, **U116**, **U117**, **U118**, **U119**, **U120**, **U121**, **U122**, **U123**, **U124**, **U125**, **U126**, **U127**, **U128**, **U129**, **U130**, **U131**, **U132**, **U133**, **U134**, **U135**, **U136**, **U137**, **U138**, **U139**, **U140**, **U141**, **U142**, **U143**, **U144**, **U145**, **U146**, **U147**, **U148**, **U149**, **U150**, **U151**, **U152**, **U153**, **U154**, **U155**, **U156**, **U157**, **U158**, **U159**, **U160**, **U161**, **U162**, **U163**, **U164**, **U165**, **U166**, **U167**, **U168**, **U169**, **U170**, **U171**, **U172**, **U173**, **U174**, **U175**, **U176**, **U177**, **U178**, **U179**, **U180**, **U181**, **U182**, **U183**, **U184**, **U185**, **U186**, **U187**, **U188**, **U189**, **U190**, **U191**, **U192**, **U193**, **U194**, **U195**, **U196**, **U197**, **U198**, **U199**, **U200**, **U201**, **U202**, **U203**, **U204**, **U205**, **U206**, **U207**, **U208**, **U209**, **U210**, **U211**, **U212**, **U213**, **U214**, **U215**, **U216**, **U217**, **U218**, **U219**, **U220**, **U221**, **U222**, **U223**, **U224**, **U225**, **U226**, **U227**, **U228**, **U229**, **U230**, **U231**, **U232**, **U233**, **U234**, **U235**, **U236**, **U237**, **U238**, **U239**, **U240**, **U241**, **U242**, **U243**, **U244**, **U245**, **U246**, **U247**, **U248**, **U249**, **U250**, **U251**, **U252**, **U253**, **U254**, **U255**, **U256**, **U257**, **U258**, **U259**, **U260**, **U261**, **U262**, **U263**, **U264**, **U265**, **U266**, **U267**, **U268**, **U269**, **U270**, **U271**, **U272**, **U273**, **U274**, **U275**, **U276**, **U277**, **U278**, **U279**, **U280**, **U281**, **U282**, **U283**, **U284**, **U285**, **U286**, **U287**, **U288**, **U289**, **U290**, **U291**, **U292**, **U293**, **U294**, **U295**, **U296**, **U297**, **U298**, **U299**, **U300**, **U301**, **U302**, **U303**, **U304**, **U305**, **U306**, **U307**, **U308**, **U309**, **U310**, **U311**, **U312**, **U313**, **U314**, **U315**, **U316**, **U317**, **U318**, **U319**, **U320**, **U321**, **U322**, **U323**, **U324**, **U325**, **U326**, **U327**, **U328**, **U329**, **U330**, **U331**, **U332**, **U333**, **U334**, **U335**, **U336**, **U337**, **U338**, **U339**, **U340**, **U341**, **U342**, **U343**, **U344**, **U345**, **U346**, **U347**, **U348**, **U349**, **U350**, **U351**, **U352**, **U353**, **U354**, **U355**, **U356**, **U357**, **U358**, **U359**, **U360**, **U361**, **U362**, **U363**, **U364**, **U365**, **U366**, **U367**, **U368**, **U369**, **U370**, **U371**, **U372**, **U373**, **U374**, **U375**, **U376**, **U377**, **U378**, **U379**, **U380**, **U381**, **U382**, **U383**, **U384**, **U385**, **U386**, **U387**, **U388**, **U389**, **U390**, **U391**, **U392**, **U393**, **U394**, **U395**, **U396**, **U397**, **U398**, **U399**, **U400**, **U401**, **U402**, **U403**, **U404**, **U405**, **U406**, **U407**, **U408**, **U409**, **U410**, **U411**, **U412**, **U413**, **U414**, **U415**, **U416**, **U417**, **U418**, **U419**, **U420**, **U421**, **U422**, **U423**, **U424**, **U425**, **U426**, **U427**

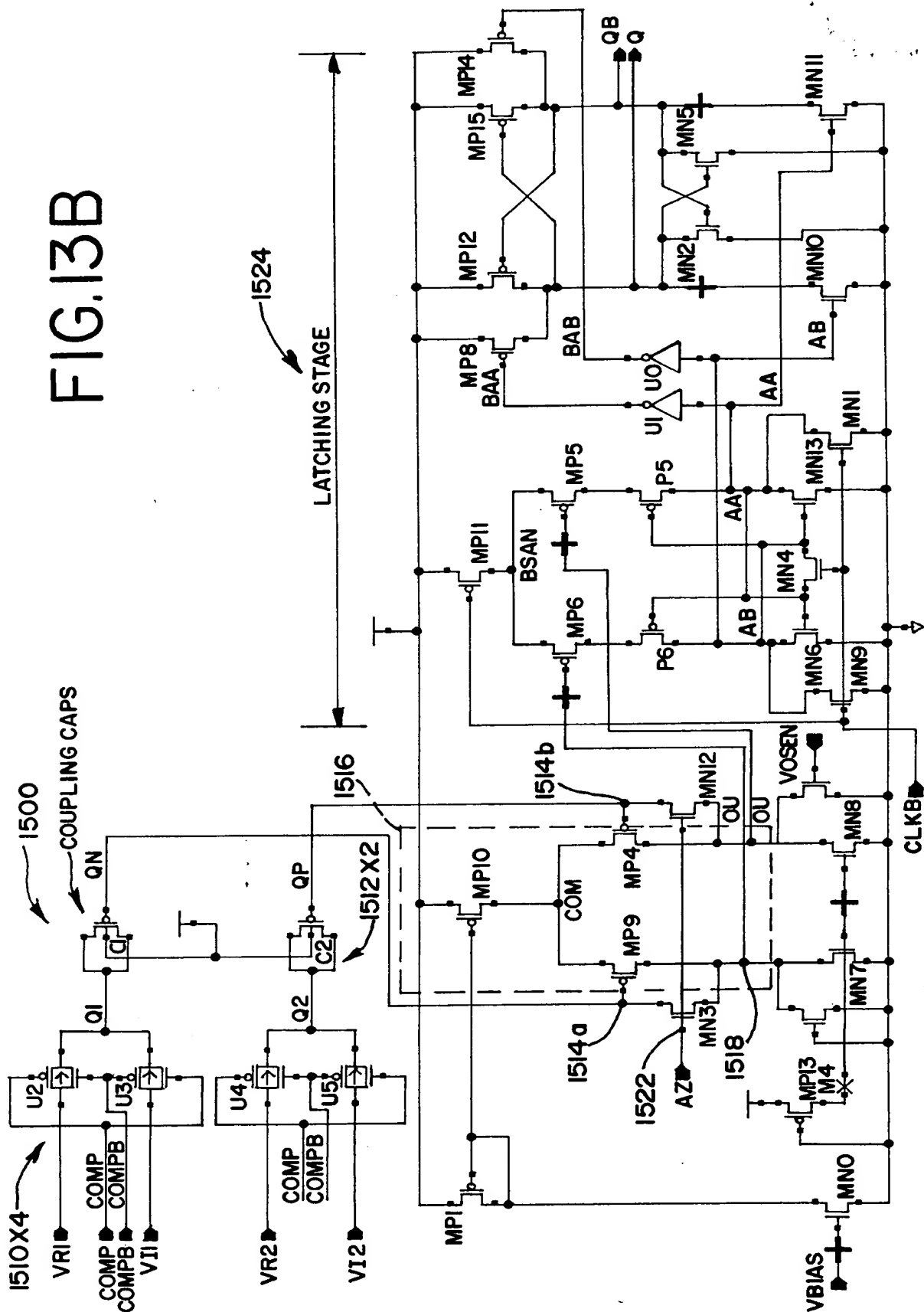
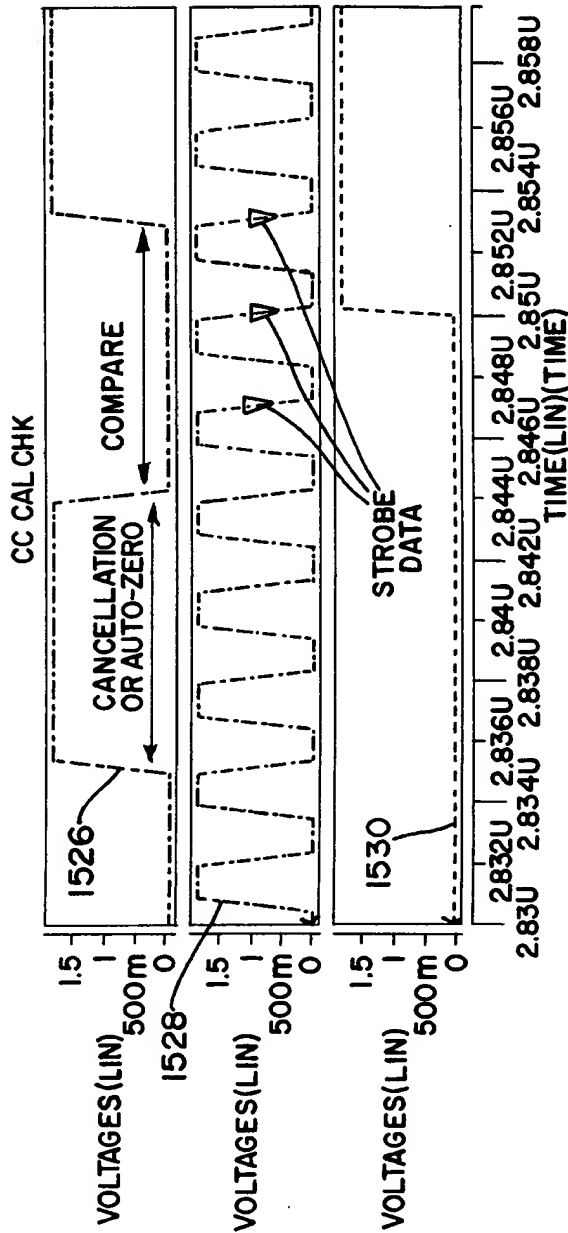
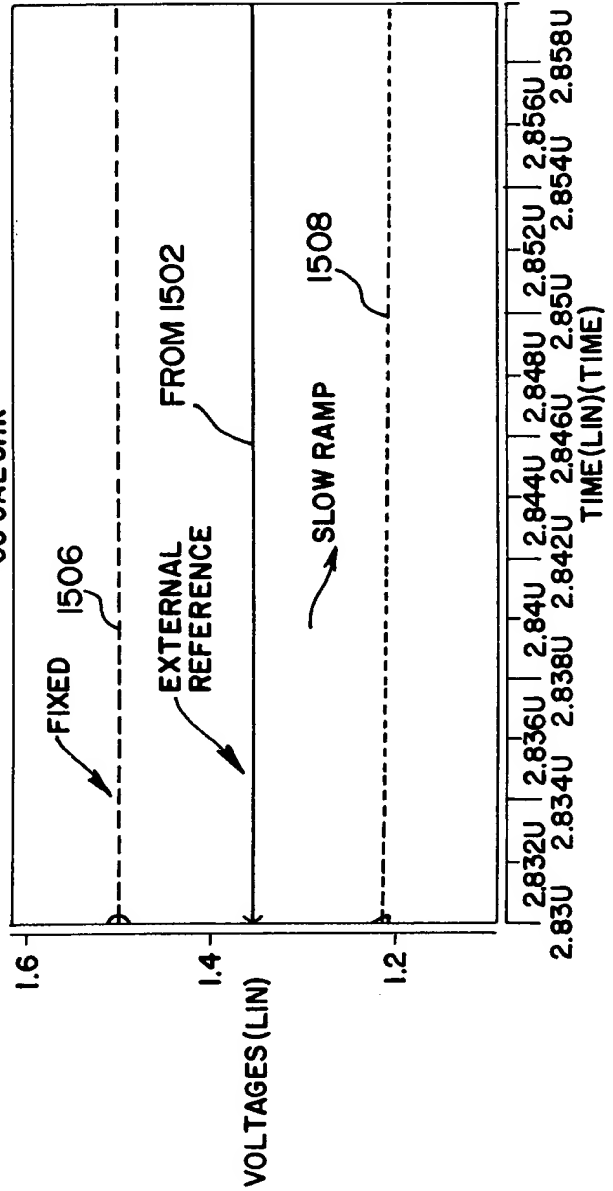


FIG. 13C

WAVE	SYMBOL
DO:AO:V(CC OUT)	---X---
DO:AO:V(DS)	---X---
DO:AO:V(AS)	---A---



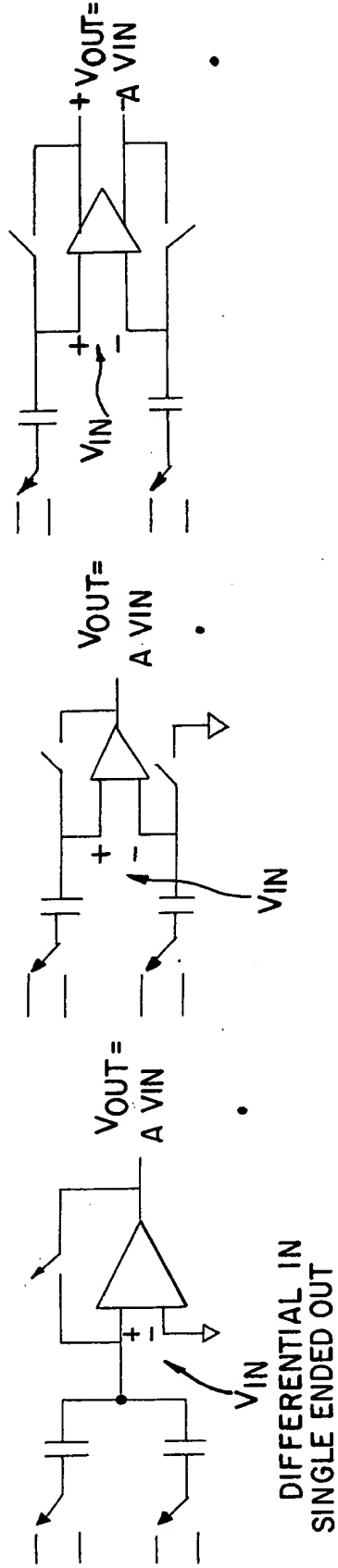
CC CALCHK



WAVE	SYMBOL
DO:AO:V(vrefm)	---X---
DO:AO:V(inhi)	---O---
DO:AO:V(inlo)	---A---

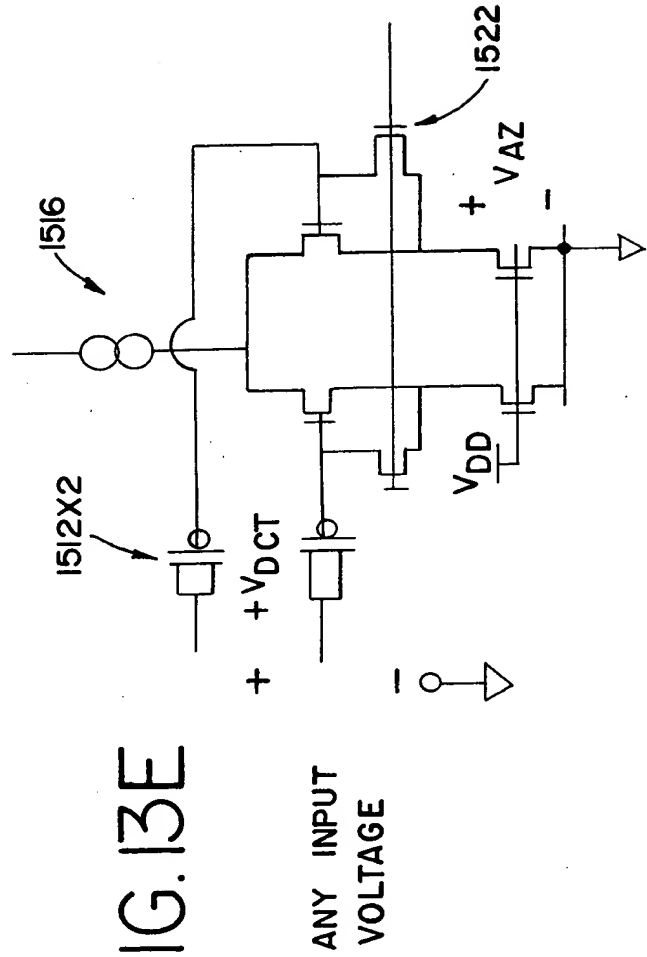
FOE280 05224260

FIG. 13D

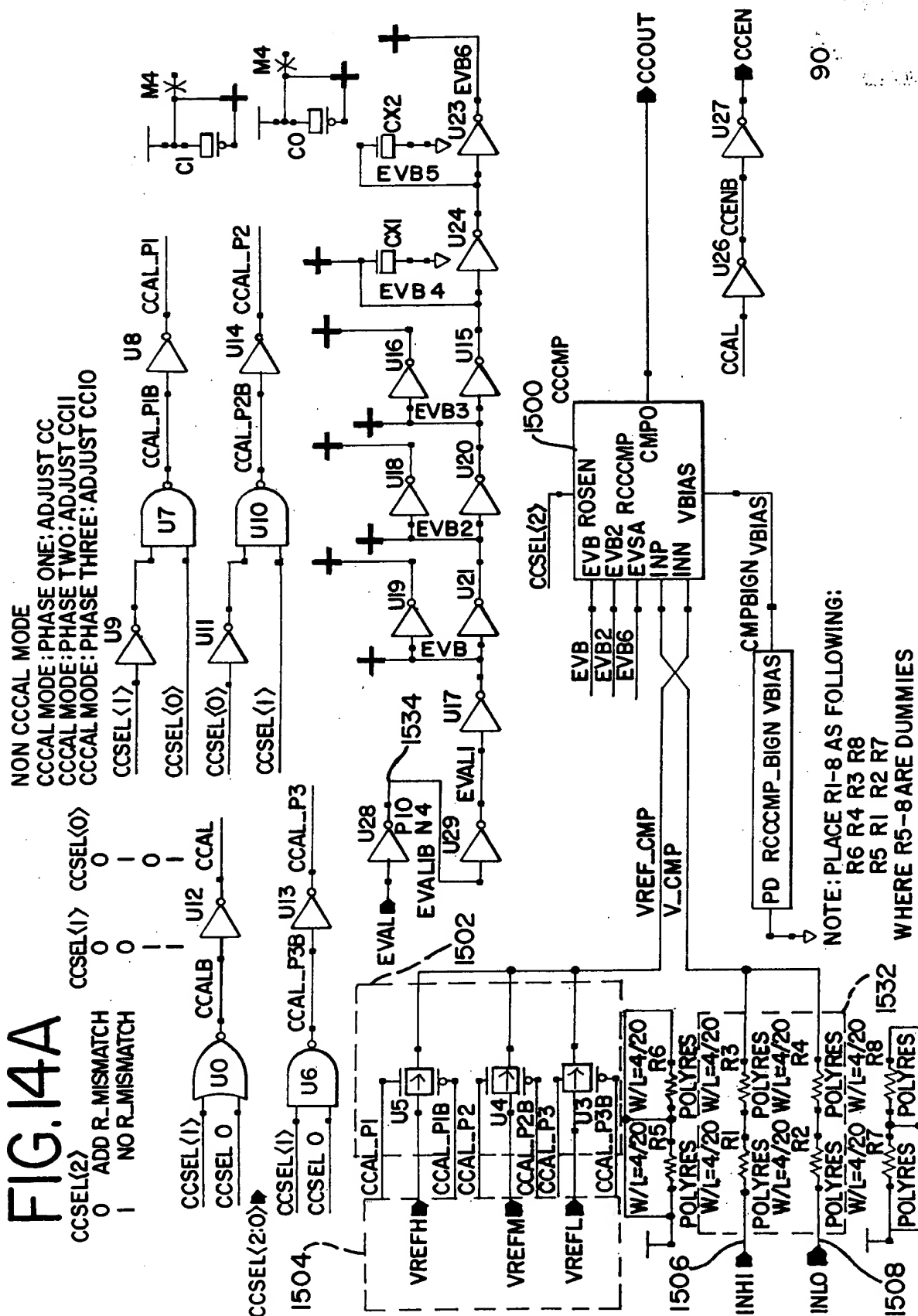


DIFFERENTIAL IN  
SINGLE ENDED OUT

FIG. 13E



## NON CCCAL MODE

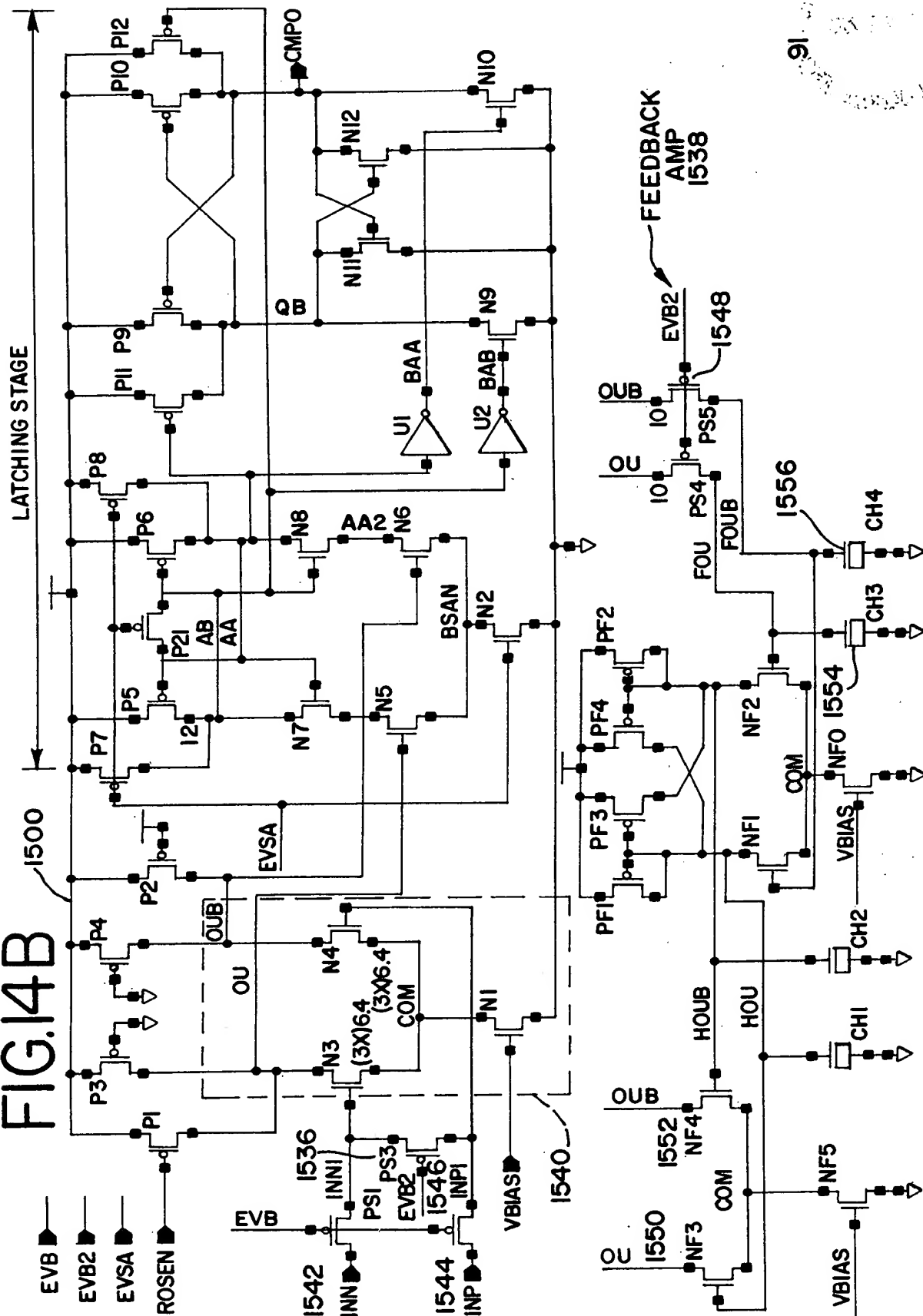


**NOTE: PLACE RI-8 AS FOLLOWING:**

WHERE R5-8 ARE DUMMIES



FIG. 4B



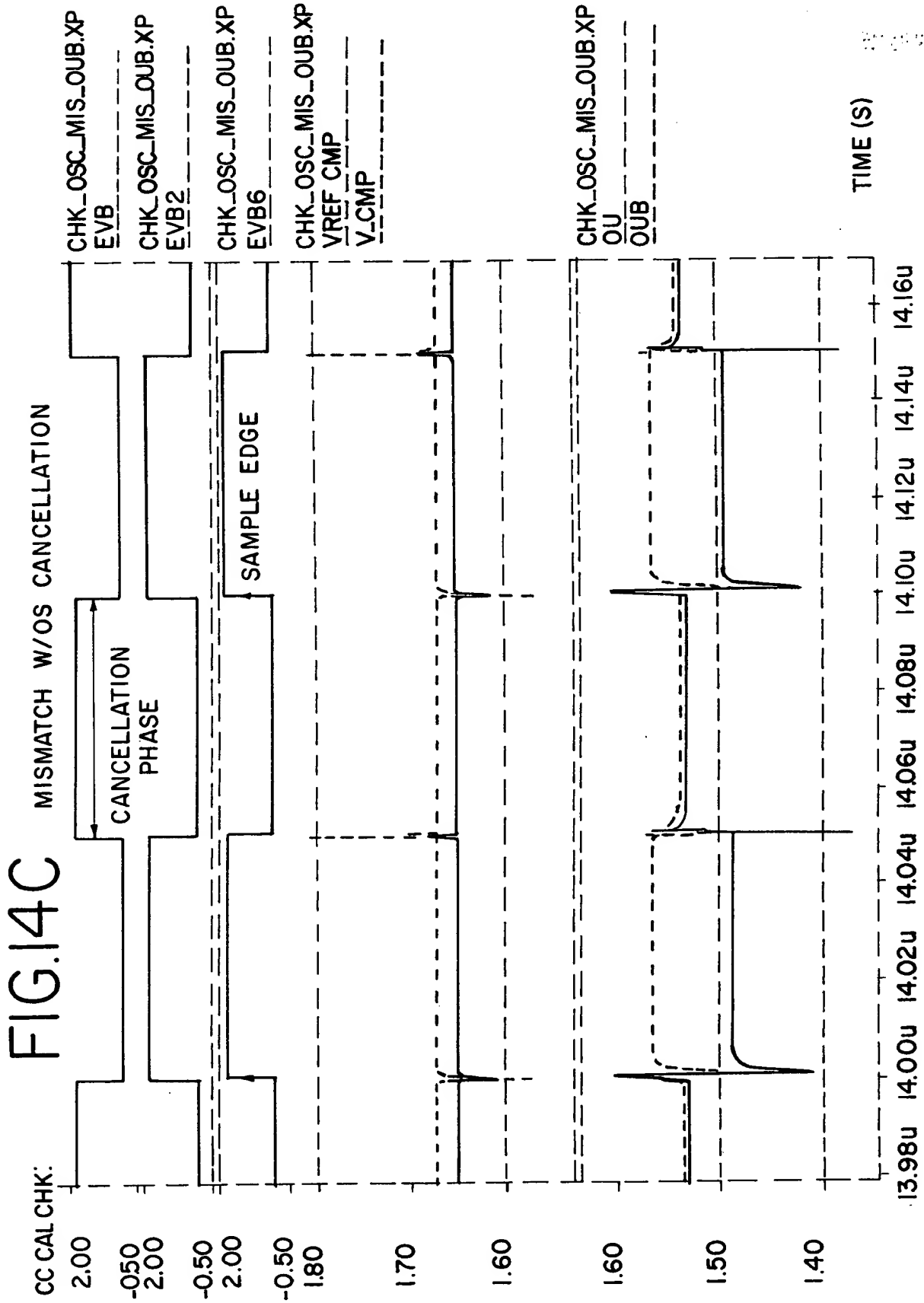


FIG. 14D

W/OS CANCEL

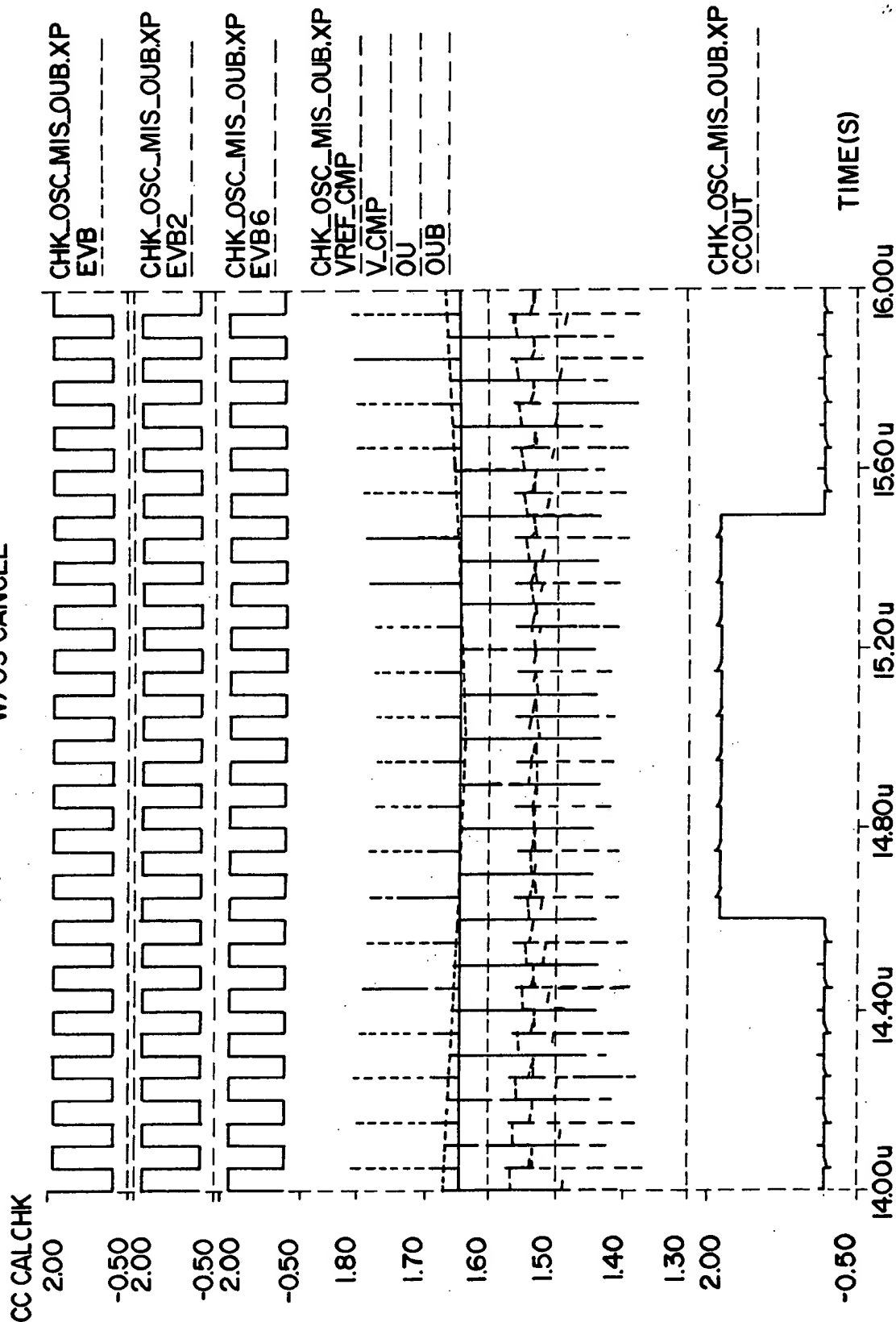


FIG.15A

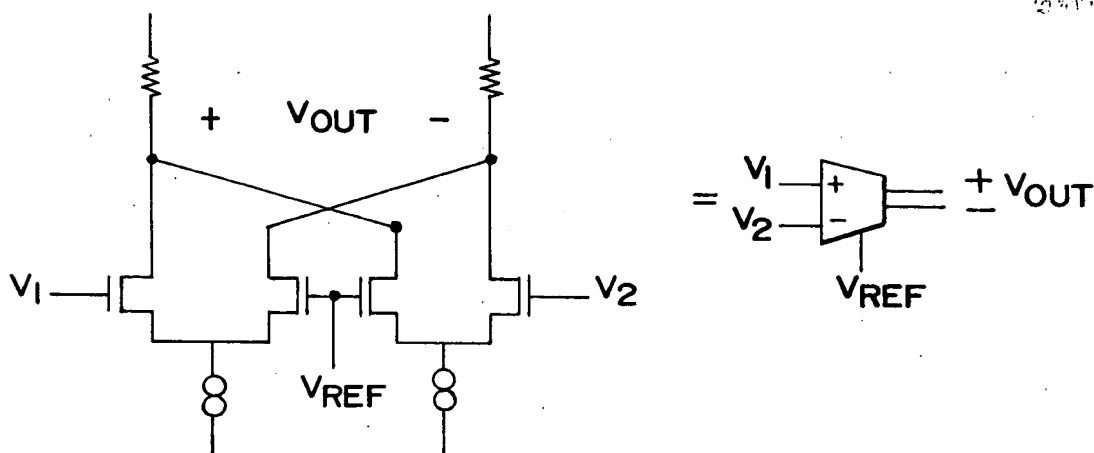


FIG.15B

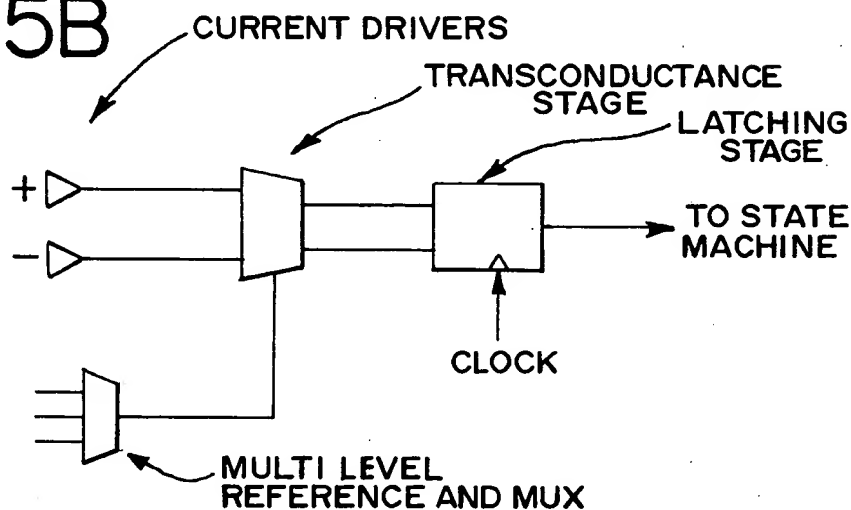


FIG.16

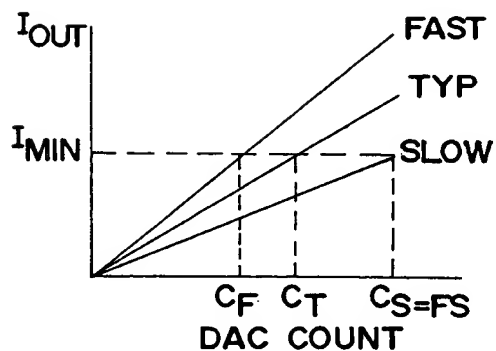
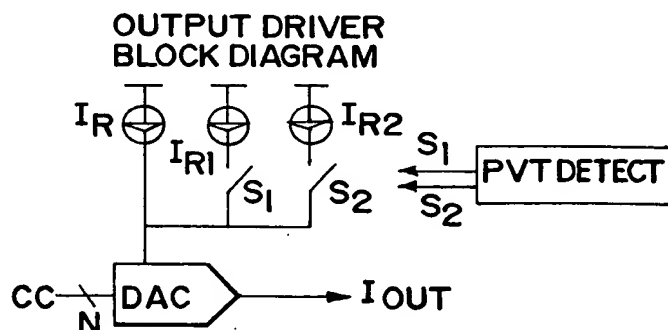


FIG.17



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FIG.18

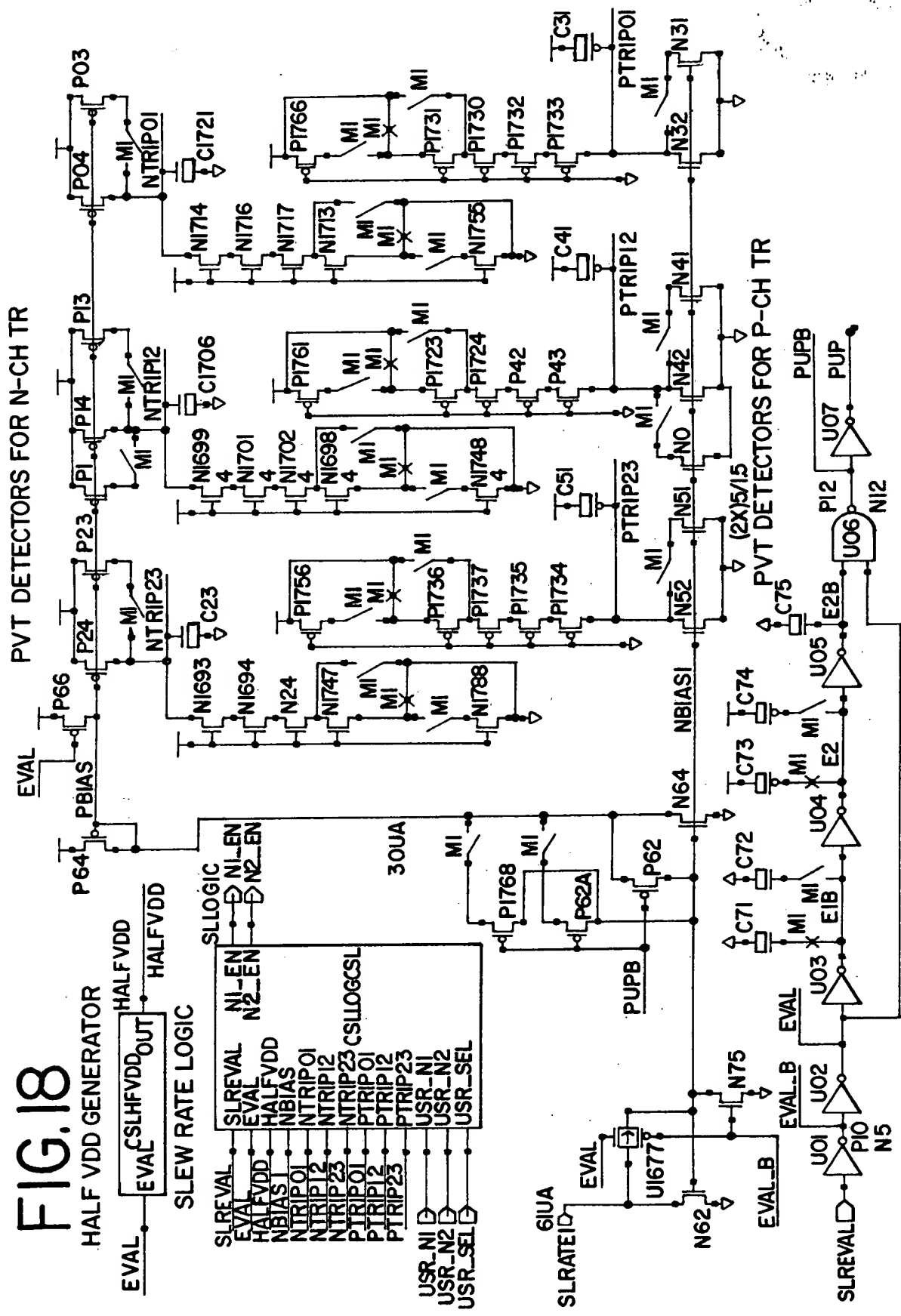


FIG. 19A

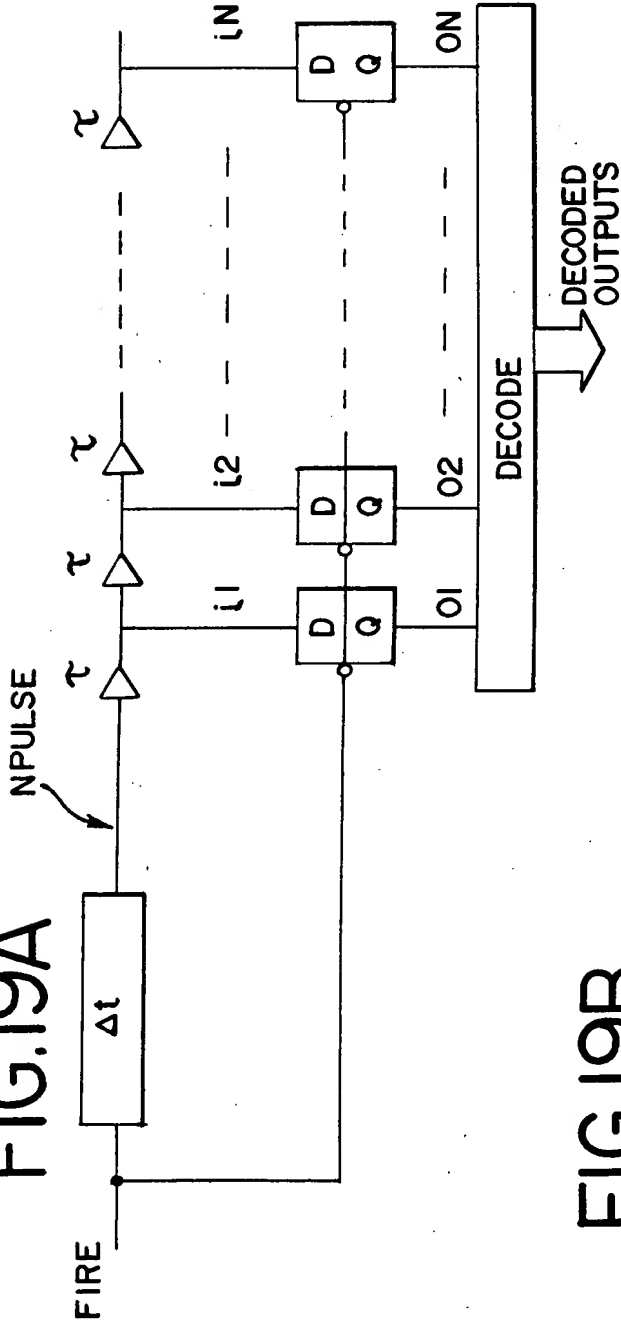


FIG. 19B

SLOW CONDITIONS

$t$  FIXED

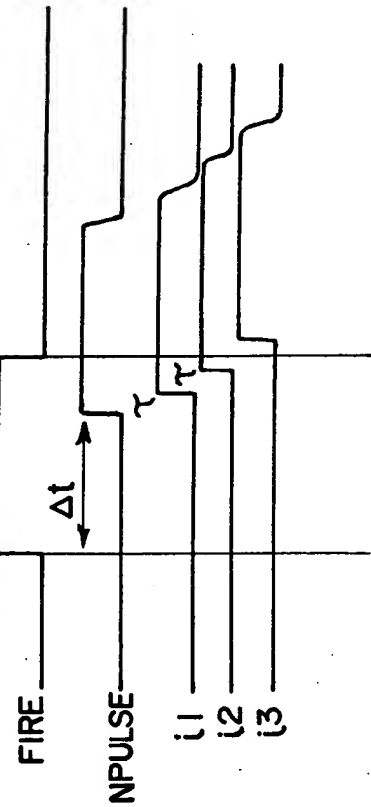


FIG. 19C

FAST CONDITIONS

$t$  FIXED

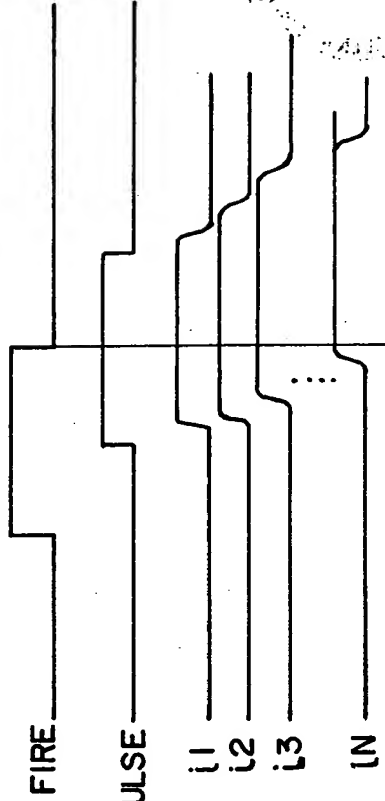
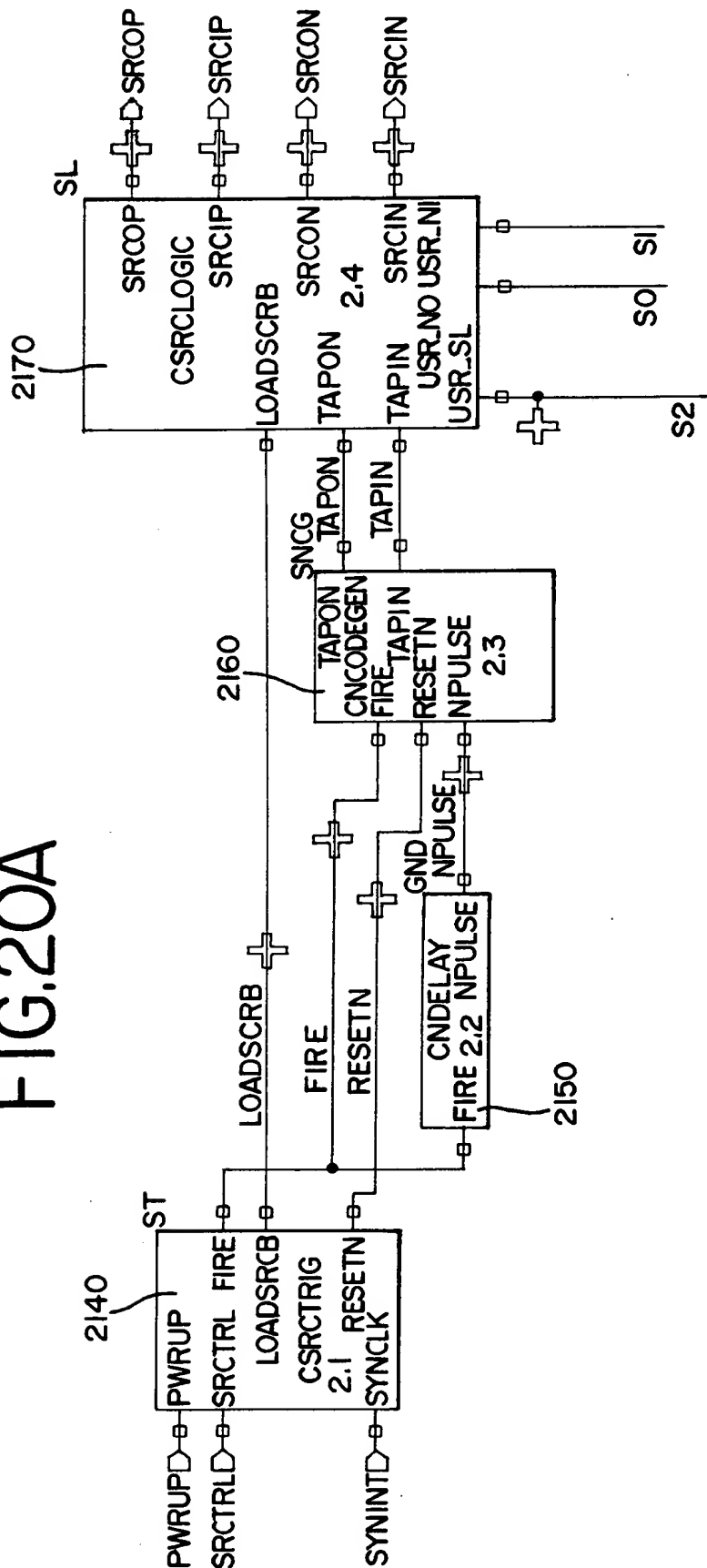


FIG. 20A



$\overline{\text{S0}}$   
 $\overline{\text{S1}}$   
 $\overline{\text{S2}}$

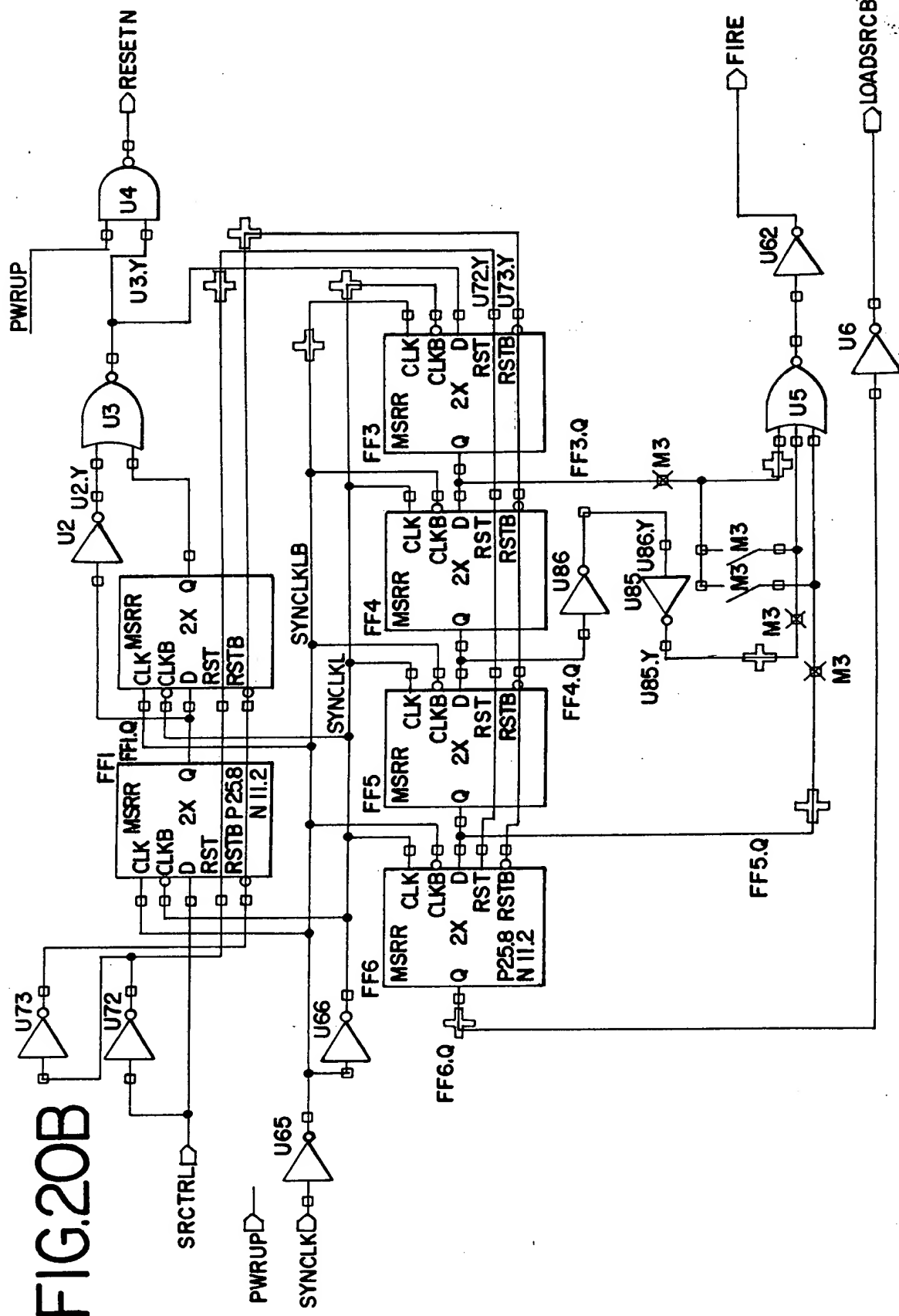




FIG.20C

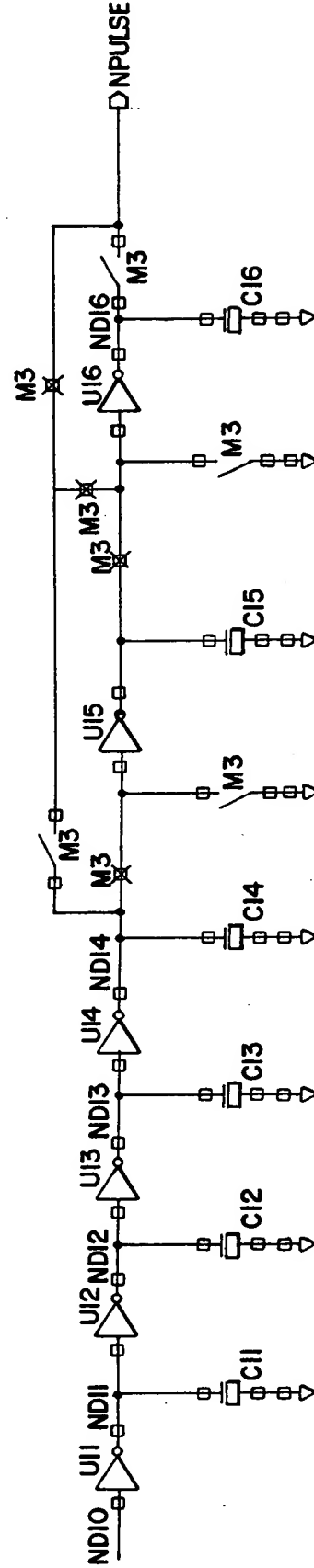
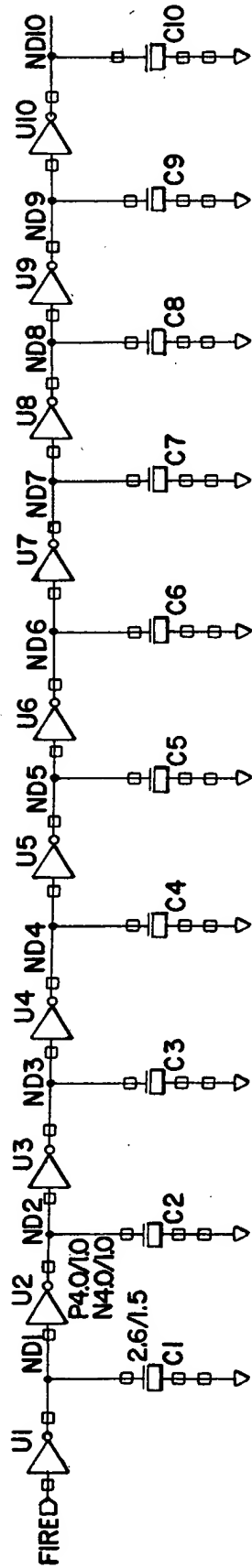


FIG. 20D

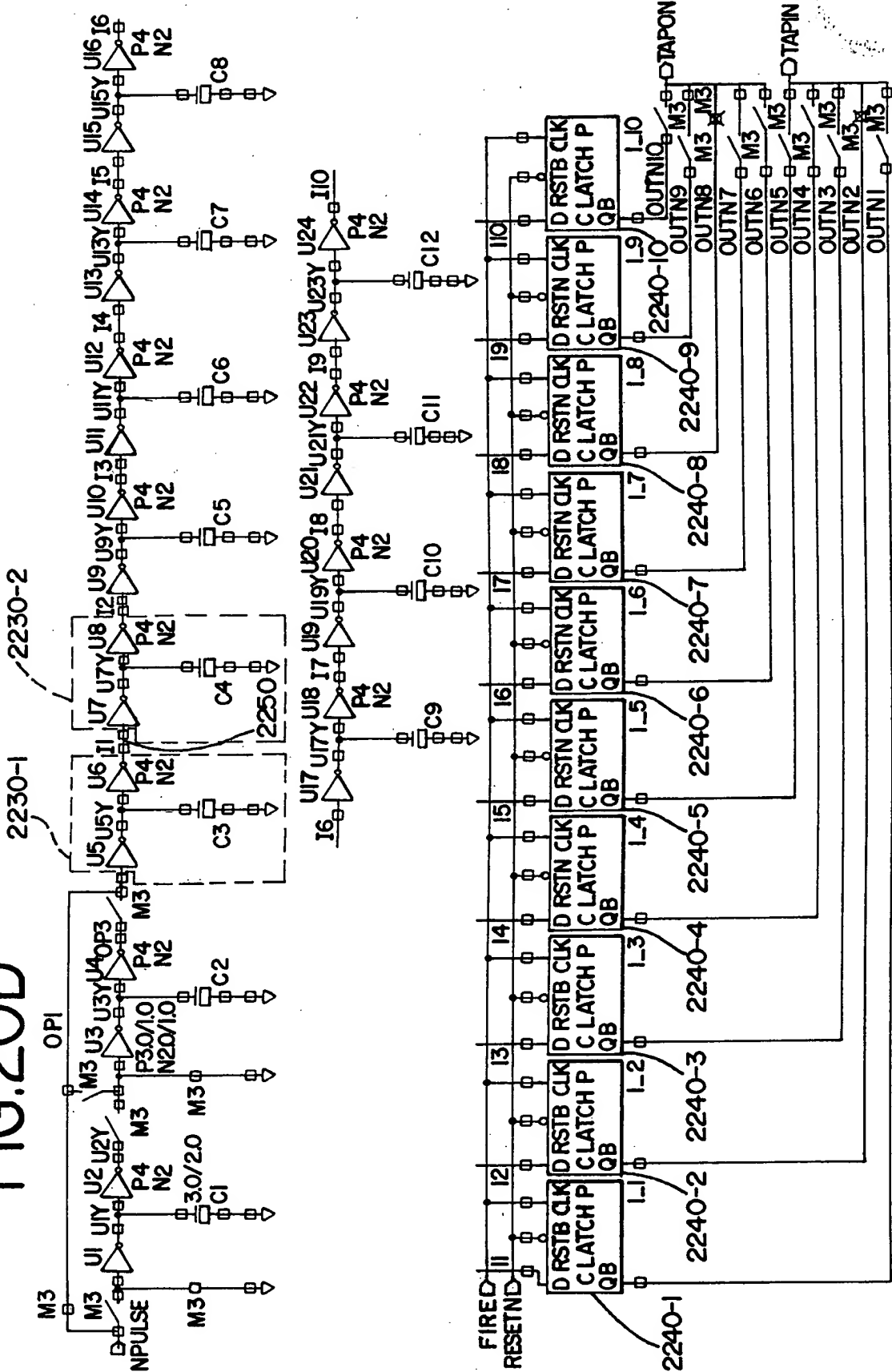


FIG.20E

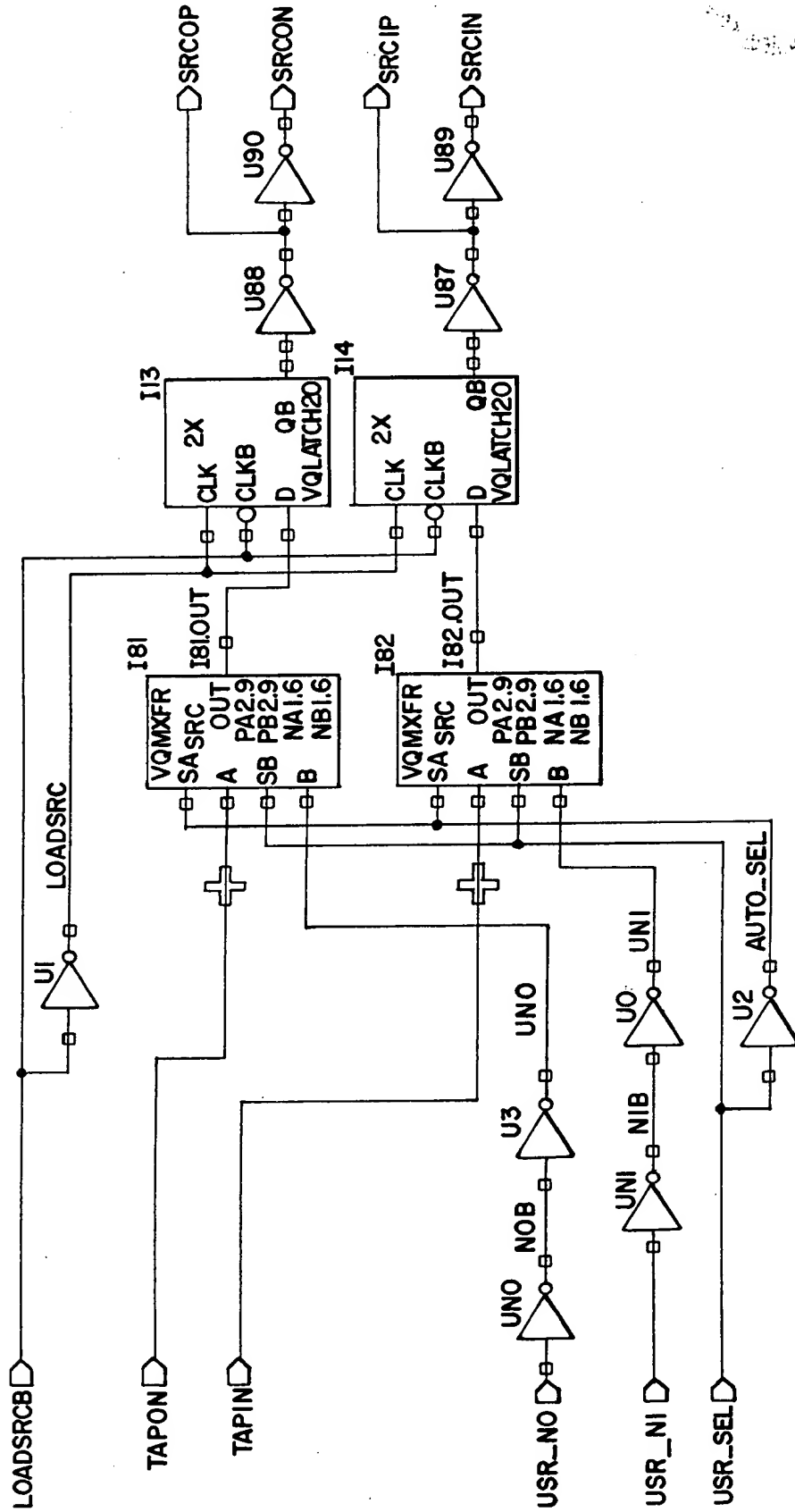


FIG.21A

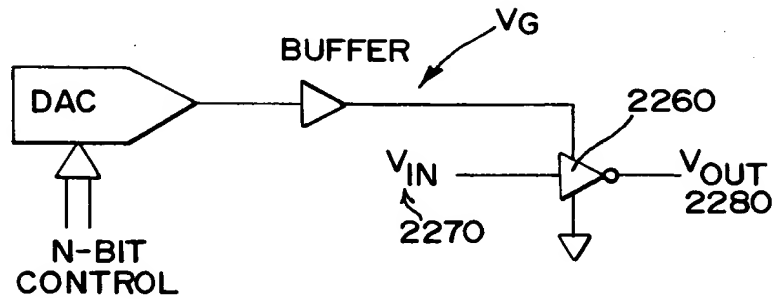


FIG.21B

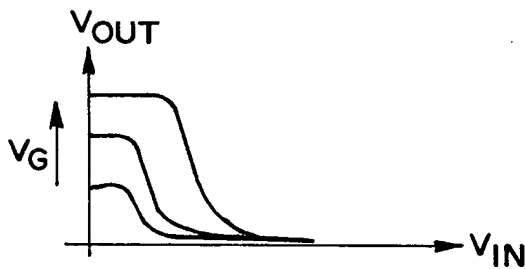


FIG.22

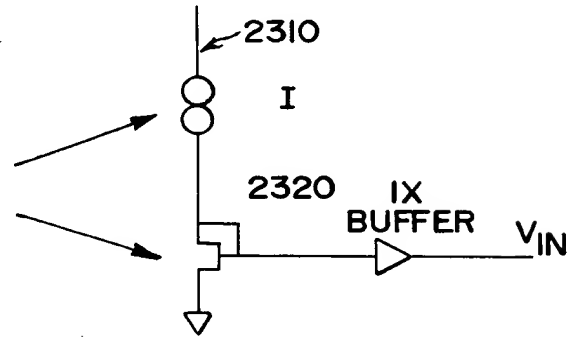
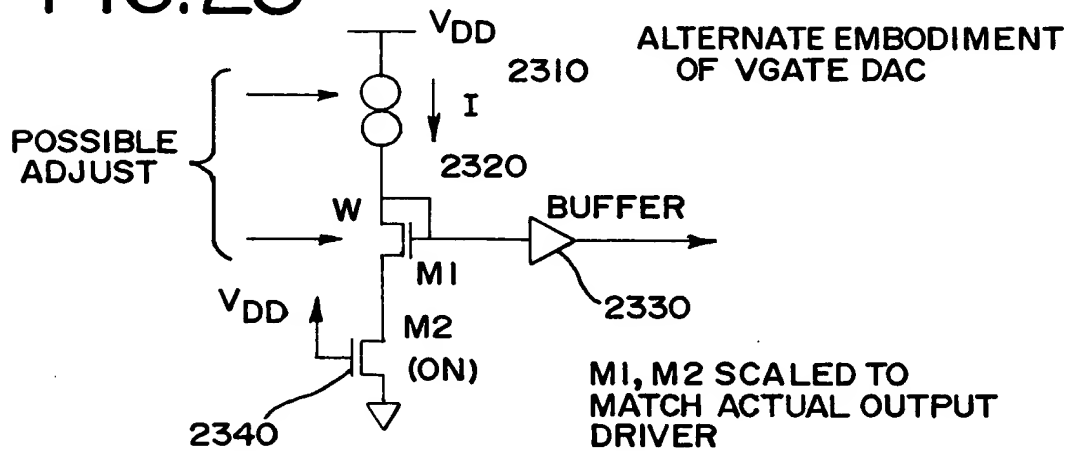
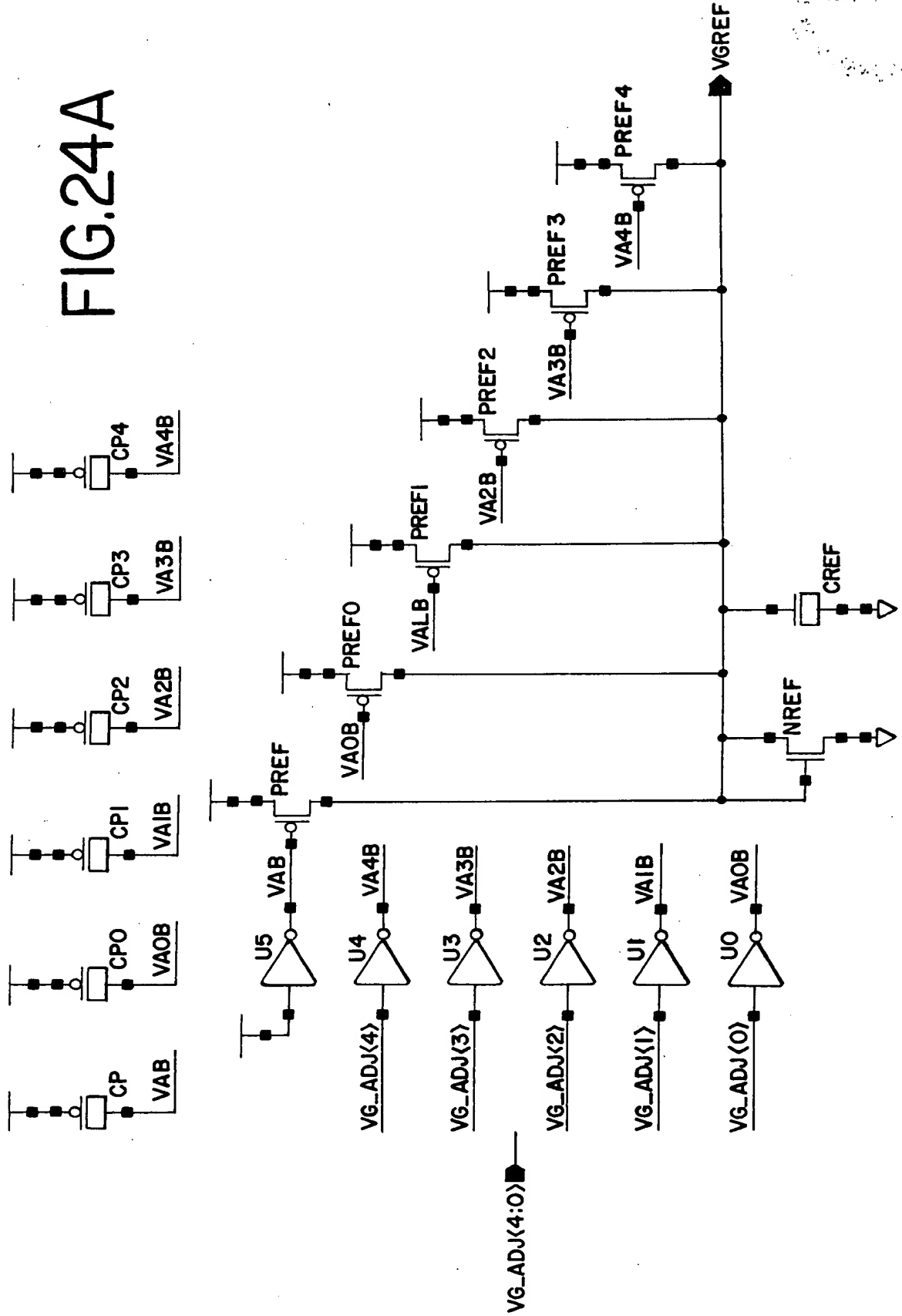


FIG.23



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FIG. 24A



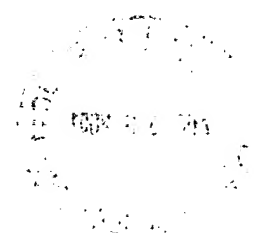


FIG.24B

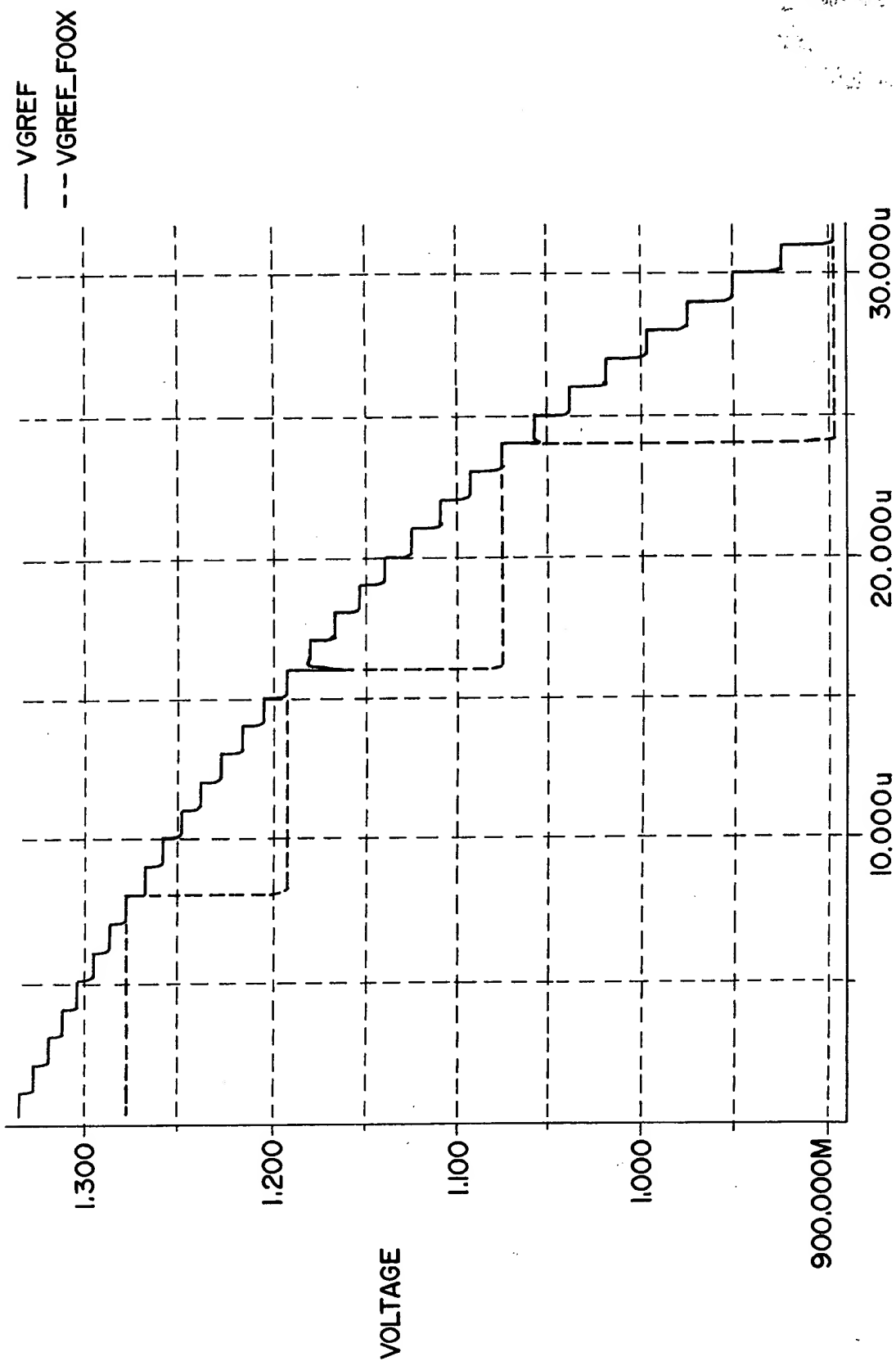


FIG.24B 05224260